

Abstraction refinement and antichains for trace inclusion of infinite state systems

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Abstract

A *generic register automaton* is a finite automaton equipped with variables (which may be viewed as counters or, more generally, registers) ranging over infinite data domains. A trace of a generic register automaton is an alternating sequence of alphabet symbols and values taken by the variables during an execution of the automaton. The problem addressed in this paper is the inclusion between the sets of traces (data languages) recognized by such automata. Since the problem is undecidable in general, we give a semi-algorithm based on a combination of abstraction refinement and antichains, which is proved to be sound and complete, but whose termination is not guaranteed. Moreover, we further enhance the proposed algorithm by exploiting a concept of data simulations, i.e., simulation relations aware of the data associated with the words. We have implemented our technique in a prototype tool and show promising results on multiple non-trivial examples.

Keywords Generic register automata · Data automata · Trace inclusion · Antichains · Interpolation · CEGAR · Simulation relations

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1 Introduction

Many results in formal languages and automata theory rely on the assumption that the alphabet over which languages are defined is finite. The finite alphabet hypothesis is crucial for the existence of determinization, complementation and decidability of language inclusion problems for the language acceptor class under consideration. However, this assumption prevents the use of automata as models of real-time systems or infinite-state programs. In general, traditional attempts to generalize classical finite-state automata to infinite alphabets, such as timed automata [\[2](#page-32-0)] or finite-memory register automata [\[26](#page-32-1)] face the complement closure problem: there exists automata for which the complement language cannot be recognized by automata in the same class. This prevents encoding language inclusion problems *L*(*A*) ⊆ *L*(*B*) as the emptiness of the language *L*(*A*) ∩ *L*(*B*), because the complement $\overline{L(B)}$ of the language $\mathcal{L}(B)$ cannot be computed within the class of *A* and *B*. Moreover, the language inclusion problem is proved to be undecidable, in general, for timed [\[2](#page-32-0)] and finite-memory [\[26](#page-32-1)] automata, unless severe restrictions are applied.

In this paper, we consider a generalization of finite-state automata, by adding finitely many variables that range over an infinite data domain and whose values are part of the language of the automaton. We address the *trace inclusion* problem between (i) a network of *generic register automata*^{[1](#page-1-0)} $A = \langle A_1, \ldots, A_N \rangle$ that communicate via a set of input events Σ_A and a set of shared variables **x***A*, ranging over an infinite data domain, and (ii) a generic register automaton *B* whose set of variables \mathbf{x}_B is a subset of \mathbf{x}_A and whose set of input events is Σ_B . Here, by a *trace*, we understand an alternating sequence of valuations of the variables from the set \mathbf{x}_B and input events from the set $\Sigma_A \cap \Sigma_B$, starting and ending with a valuation. Typically, the automata network *A* models the implementation of a concurrent system and *B* is a specification of the set of good behaviors of the system. Then, a positive answer to the above inclusion problem means that the behavior of the implementation conforms to the specification, which is a natural verification problem.

Consider, for instance, the network $\langle A_1, \ldots, A_N \rangle$ of generic register automata equipped with the integer-valued variables x and v shown in Fig. [1–](#page-2-0)left. The automata synchronize on the **init** symbol and interleave their $\mathbf{a}_{1,\dots,N}$ actions. Each automaton A_i increases the shared variable x and writes its identifier i into the shared variable v as long as the value of *x* is in the interval $[(i - 1)\Delta, i\Delta - 1]$, and it is inactive outside this interval, where $\Delta \geq 1$ is an unbounded parameter of the network. A possible specification for this network might require that each firing sequence is of the form **init** $a_{1,...,N}^* a_2 a_{2,...,N}^* \dots a_i a_i^*$ for some $1 \leq i \leq N$, and that v is increased only on the first occurrence of the events $\mathbf{a}_2, \ldots, \mathbf{a}_i$, in this order. This condition is encoded by the automaton *B* (Fig. [1–](#page-2-0)right). Observe that only the v variable is shared between the network $\langle A_1, \ldots, A_N \rangle$ and the specification automaton *B*—we say that v is *observable* in this case. An example of a trace, for $\Delta = 2$ and $N \ge 3$, is: $(v = 0)$ **init** $(v = 1)$ **a**₁ $(v = 1)$ **a**₁ $(v = 1)$ **a**₂ $(v = 2)$ **a**₂ $(v = 2)$ **a**₃ $(v = 3)$. Our problem is to check that this, and all other traces of the network, are included in the language of the specification automaton, called the *observer*. The trace inclusion problem has multiple applications, e.g.:

– Decision procedures for logics describing array structures within imperative programs [\[18](#page-32-2)[,19\]](#page-32-3) that use a translation of array formulae to integer counter automata which encode the set of array models of a formula. The expressiveness of such logics is currently limited by the undecidability of the emptiness (reachability) problem for counter automata. If

¹ Generic register automata were called data automata in our preliminary work $[24]$ $[24]$. We have decided to change the name in order to avoid confusion with some other formalisms.

Fig. 1 An instance of the trace inclusion problem

we give up on decidability, we can reduce an entailment between two array formulae to the trace inclusion of two integer counter automata, and use the method presented in this paper as a semi-decision procedure. To corroborate this claim, we have applied our trace inclusion method to several verification conditions for programs with unbounded arrays of integers [\[8](#page-32-5)].

– Timed automata and regular specifications of timed languages [\[2\]](#page-32-0) can be both represented by finite automata extended with real-valued variables [\[16](#page-32-6)]. The verification problem boils down to the trace inclusion of two real-valued generic register automata.^{[2](#page-2-1)} In this context, our method has been tested on several timed verification problems, including communication protocols and boolean circuits [\[34](#page-33-0)].

When developing a method for checking the inclusion between trace languages of automata extended with variables ranging over infinite data domains, the first problem is the lack of determinization and/or complementation results. In fact, certain classes of infinite state systems, such as finite-memory (register) [\[26\]](#page-32-1) or timed automata [\[2\]](#page-32-0), cannot be determinized and are provably not closed under complement. This is the case due to the fact that the values of the variables (registers, clocks) in such models of automata are not observable in the recognized language, that is determined by a series of internal computations.

However, if we allow the values of all variables of a generic register automaton to be part of its trace language, we obtain a determinization result, which generalizes the classical subset construction by taking into account the data valuations. Building on this first result, we define the complement of a trace language as an effectively computable generic register automaton. Thus, we can reduce the trace inclusion problem to the emptiness of a generic register product automaton $\mathcal{L}(A \times \overline{B}) = \emptyset$, just as in the finite alphabet case. However, the reduction of the trace inclusion to the emptiness problem crucially relies on the fact that the variables \mathbf{x}_B of the right-hand side generic register automaton *B* (the one being determinized) are also controlled by the left-hand side automaton *A*, in other words, that *B* has no hidden variables. It is still an open problem whether and in which circumstances this reduction can be achieved in the presence of hidden variables.

The language emptiness problem for generic register automata is, in general, undecidable [\[30\]](#page-32-7). Nevertheless, several semi-algorithms and tools for this problem, better known as the *reachability* problem, have been developed [\[3](#page-32-8)[,17](#page-32-9)[,22](#page-32-10)[,27](#page-32-11)]. Among those, the technique of *lazy predicate abstraction* [\[22](#page-32-10)] combined with *counterexample-driven refinement* using *interpolants*[\[27](#page-32-11)] has been shown to be particularly successful in proving emptiness of infinite-state systems. Moreover, this technique shares similar aspects with *antichain-based algorithms for language inclusion* in the case of a finite alphabet [\[1](#page-32-12)[,35](#page-33-1)]. An important similarity is that both techniques use partial orders over the set of symbolic states, to prune the search space, by storing only incomparable such states. In other words, the successors of a covered state (in the sense of the partial order) are never explored, because any counterexample that could potentially arise from that state, can also be discovered by expanding the state that covers it.

² Note that the presented trace inclusion method can be used with any data domain supported by the underlying SMT solver including integers or reals.

Even if the trace inclusion problem can be reduced, under some conditions, to the emptiness of a counter automaton for which practical semi-algorithms exist, building the entire product counter automaton before checking its emptiness is usually not feasible. This is because the size of the product automaton is exponentially larger than the sum of the sizes of *A* and *B*, even when the trace alphabet is finite. Having, moreover, an infinite alphabet adds to the size of the product automaton, obtained by a generalization of the classical subset construction, used for determinization, in the finite alphabet case. Altogether, this prevents us from directly applying state-of-the-art methods and tool for checking emptiness of counter automata, or equivalently, nondeterministic integer programs, such as constrained Horn clause solvers [\[5\]](#page-32-13).

To tackle this problem, we developed a semi-algorithm that builds the product automaton on-the-fly, while checking its emptiness. We achieve practical efficiency by combining the principle of antichain-based language inclusion algorithms [\[1](#page-32-12)[,35\]](#page-33-1) with the interpolant-based abstraction refinement semi-algorithm [\[27\]](#page-32-11), via a general notion of language-based subsumption relation. This semi-algorithm has been first presented in our work [\[24\]](#page-32-4). Compared with that work, this paper includes more details and also proofs of the results.

Moreover, here we introduce a notion of *data simulations*, i.e., simulation relations on generic register automata, inspired by [\[29\]](#page-32-14), and provide an algorithm to compute them. Further, we show how data simulations can be integrated into our trace inclusion semialgorithm in order to improve its performance as done previously in the context of classical finite-alphabet automata [\[1](#page-32-12)].

We have implemented the trace inclusion semi-algorithm as well as its combination with data simulations in a prototype tool INCLUDER $³$ and carried out a number of experiments,</sup> involving hardware, real-time systems, and array logic problems. The advantage of having a trace inclusion semi-algorithm is that we can write small automata-like specifications of the sets of good traces, instead of using, generally more complex, specifications of sets of erroneous behaviors.

1.1 An overview of the approach

We introduce the reader to our trace inclusion method by means of an example. Let us consider the network of generic register automata $\langle A_1, A_2 \rangle$ and the generic register automaton *B* from Fig. [1.](#page-2-0) We prove that, for any value of Δ , any trace of the network $\langle A_1, A_2 \rangle$, obtained as an interleaving of the actions of A_1 and A_2 , is also a trace of the observer B . To this end, our procedure will fire increasingly longer sequences of input events, in search for a counterexample trace. We keep a set of predicates associated with each state $(\langle q_1, q_2 \rangle, P)$ of the product automaton where q_i is a state of A_i and P is a set of states of B . These predicates^{[4](#page-3-1)} are formulae that define over-approximations of the data values reached simultaneously by the network, when A_i is in the state q_i , and by the observer B , in every state from P .

The first input event is **init**, on which A_1 and A_2 synchronize, moving together from the initial state $\langle q_0^1, q_0^2 \rangle$ to $\langle q_1^1, q_1^2 \rangle$. In response, *B* can choose to either (i) move from $\{p_0\}$ to $\{p_1\}$, matching the only transition rule from p_0 , or (ii) does not match the transition rule and move to the empty set.^{[5](#page-3-2)}

³ [http://www.fit.vutbr.cz/research/groups/verifit/tools/includer/.](http://www.fit.vutbr.cz/research/groups/verifit/tools/includer/)

⁴ Note that there is not a fixed set of predefined predicates. New predicates are discovered during refinement phase.

⁵ This option covers the case of data values allowed by the network $\langle A_1, A_2 \rangle$ that are not covered by a data constraint of any *B*-transition.

Fig. 2 A sample run of the proposed semi-algorithm

In the first case, the values of v match the relation of the rule $p_0 \xrightarrow{\text{init}, v'=1} p_1$, while in the second case, these values match the negated relation $\neg (v' = 1)$. The second case is impossible because the action of the network requires $x' = 0 \land v' = 1$. The only successor state is thus $(\langle q_1^1, q_1^2 \rangle, \{p_1\})$ in Fig. [2a](#page-4-0). Since no predicates are initially available at this state, the best over-approximation of the set of reachable data valuations is the universal set, denoted as T.

The second input event is \mathbf{a}_1 , on which A_1 moves from q_1^1 back to itself, while A_2 makes an idle step because no transition with \mathbf{a}_1 is enabled from q_1^2 . Again, *B* has the choice between moving from $\{p_1\}$ either to Ø or $\{p_1\}$. Let us consider the first case, in which the successor state is $(\langle q_1^1, q_1^2 \rangle, \emptyset, \top)$. Since q_1^1 and q_1^2 are final states of A_1 and A_2 , respectively, and no final state of B is present in \emptyset , we say that the state is accepting. If the accepting state (in dashed boxes in Fig. [2\)](#page-4-0) is reachable according to the transition constraints along the input sequence **init.a**₁, we have found a counterexample trace that is in the language of $\langle A_1, A_2 \rangle$ but not in the language of *B*.

To verify the reachability of the accepting state, we check the satisfiability of the path formula corresponding to the composition of the transition constraints $\theta_1 \equiv x' = 0 \land v' = 1$ (init) and $\theta_2 \equiv 0 \le x < \Delta \wedge x' = x + 1 \wedge v' = 1 \wedge \neg(v' = v)$ (a₁) in Fig. [2a](#page-4-0). The formula $\theta_1 \wedge \theta_2$ is unsatisfiable, and the proof of infeasibility provides the interpolant $\langle v = 1 \rangle$. This

formula is an explanation for the infeasibility of the path because it is implied by the constraint θ_1 and it is unsatisfiable in conjunction with the constraint θ_2 . By associating the new predicate $v = 1$ with the state $(\langle q_1^1, q_1^2 \rangle, \{p_1\})$, we ensure that the same spurious path will never be explored again.

We delete the spurious counterexample and recompute the states along the input sequence **init**.**a**₁ with the new predicate. In this case, $(\langle q_1^1, q_1^2 \rangle, \emptyset)$ is unreachable, and the outcome is $(\langle q_1^1, q_1^2 \rangle, \{p_1\}, v = 1)$. However, this state was first encountered after the sequence **init**, so there is no need to store a second occurrence of this state in the tree. We say that the node **init.** a_1 is subsumed by **init**, and indicate this by a dashed arrow in Fig. [2b](#page-4-0).

We continue with \mathbf{a}_2 from the state $(\langle q_1^1, q_1^2 \rangle, \{p_1\}, v = 1)$. In this case, A_1 makes an idle step and A_2 moves from q_1^2 to itself. In response, *B* has the choice between moving from $\{p_1\}$ to either (i) $\{p_1\}$ with the constraint $v' = v$, (ii) $\{p_2\}$ with the constraint $v' = v + 1$, (iii) $\{p_1, p_2\}$ with the constraint $v' = v \wedge v' = v + 1$ (this constraint is unsatisfiable, hence this case is discarded), (iv) Ø for data values that satisfy $\neg(v' = v) \land \neg(v' = v + 1)$.

Cases (i) and (iv) are also discarded because the value of v after **init** is constrained to 1 and *A*₂ further imposes the constraint $v' = 2$. All together, $v = 1 \wedge v' = 2 \wedge v' = v$ is unsatisfiable within Case (i) and $v = 1 \wedge v' = 2 \wedge \neg(v' = v) \wedge \neg(v' = v + 1)$ is unsatisfiable within Case (iv). Hence, the only **a**₂-successor of $(\langle q_1^1, q_1^2 \rangle, \{p_1\}, v = 1)$ is $(\langle q_1^1, q_1^2 \rangle, \{p_2\}, \top)$, in Fig. [2b](#page-4-0).

By firing the event \mathbf{a}_1 from this state, we reach $(\langle q_1^1, q_1^2 \rangle, \emptyset, v = 1)$, which is, again, accepting. We check whether the path **init**.**a**2.**a**¹ is feasible, which turns out not to be the case. For efficiency reasons, we find the shortest suffix of this path that can be proved infeasible. It turns out that the sequence $\mathbf{a}_2 \cdot \mathbf{a}_1$ is infeasible starting from the state $(\langle q_1^1, q_1^2 \rangle, \{p_1\}, v = 1)$, which is called the *pivot*. This proof of infeasibility yields the interpolant $\langle v = 1, \Delta < x \rangle$, and a new predicate $\Delta < x$ is associated with $(\langle q_1^1, q_1^2 \rangle, \{p_2\})$. The refinement phase rebuilds only the subtree rooted at the pivot state, in Fig. [2b](#page-4-0).

The procedure then builds the tree in Fig. [2c](#page-4-0) starting from the pivot node and finds the accepting state $((q_1^1, q_1^2), \emptyset, \Delta < x)$ as the result of firing the sequence **init**. $\mathbf{a}_2 \cdot \mathbf{a}_2$. This path is spurious, and the new predicate $v = 2$ is associated with the location $(\langle q_1^1, q_1^2 \rangle, \{p_2\})$. The pivot node is the same as in Fig. [2b](#page-4-0), and, by recomputing the subtree rooted at this node with the new predicates, we obtain the tree in Fig. [2d](#page-4-0), in which all frontier nodes are subsumed by their predecessors. Thus, no new event needs to be fired, and the procedure can stop reporting that the trace inclusion holds.

1.2 Related work

Extending automata to deal with infinite alphabets is the purpose of the seminal work of Kaminski and Francez [\[26](#page-32-1)], who introduce *finite-memory automata* that accept languages over infinite alphabets using a finite set of registers that can be overwritten and compared for equality with the input. In addition, our model of generic register automata is parametric in the theory of the data used and allows comparisons between adjacent elements in the input stream. For instance, generic register automata can easily specify increasing sequences of integers, which is out of the scope of finite-memory automata. Moreover, the language inclusion problem is undecidable for finite-memory automata, if the right-hand side has more than 2 registers, while decidability is proved for at most 2 registers.

The trace inclusion problem has also been addressed in the context of timed automata [\[32\]](#page-33-2). Although the problem $\mathcal{L}(A) \subseteq \mathcal{L}(B)$ is undecidable in general [\[2](#page-32-0)], decidability is recovered when the *B* automaton has at most one clock, or the only constant appearing in the clock constraints is zero. These are essentially the only known decidable cases of language inclusion for timed automata.

The study of *data automata* [\[6](#page-32-15)[,14](#page-32-16)] usually deals with the complexity of decision problems in logics describing data languages for simple theories, typically infinite data domains with equality. Here, we focus on undecidable language inclusion problems between data automata controlled by generic first-order theories, by providing a semi-algorithm that proves to be effective, in practice.

Data words have also been considered in the context of *symbolic visibly pushdown automata* (SVPA) [\[13](#page-32-17)]. Language inclusion is decidable for SVPAs with transition guards from a decidable theory because SVPAs are closed under complement and the emptiness can be reduced to a finite number of queries expressible in the underlying theory. Decidability comes here at the cost of reducing the expressivity and forbidding comparisons between adjacent positions in the input—here only comparisons between matching call/return positions of the input nested words are allowed.

Although trace inclusion cannot be reduced to the emptiness problem of automata from the same class in linear time, due to the exponential blowup caused by determinization, this is possible if one considers an alternating automaton model, such as the one introduced in [\[25\]](#page-32-18). This work generalizes from the trace inclusion problems considered in this paper, by considering unrestricted alternation. As an advantage, one can complement in linear time without the need for determinization. On the negative side, however, the emptiness check for alternating automata with variables is heavier than in our case because it relies on the ability of the SMT solver to answer queries in a combined theory of data and booleans. Due to this reason, on some test cases, the semi-algorithm [\[25\]](#page-32-18) performs slower than the trace inclusion semi-algorithm presented here.

Several works on model checking infinite-state systems against CTL [\[4](#page-32-19)] and CTL* [\[11\]](#page-32-20) specifications are related to our problem as they check inclusion between the set of computation trees of an infinite-state system and the set of trees defined by a branching temporal logic specification. First, the verification of existential CTL formulae [\[4\]](#page-32-19) is reduced to solving forall-exists quantified Horn clauses by applying counterexample guided refinement to discover witnesses for existentially quantified variables. It is however not clear whether and how Horn clause solvers could be used for trace inclusion, which is a typical linear-time property, that requires an unbounded number of computation branches to synchronize on the same input word. To a very limited extent, for alphabets consisting of one symbol, one can encode the (non-)emptiness of alternating automata as the existence of solutions of a system of Horn clauses [\[10,](#page-32-21) Sect. 7.2.3]. However this encoding fails for alphabets of size two or more, let alone for infinite data alphabets. Moreover, we have not encountered a polynomial-time encoding of trace inclusion as a system of Horn clauses in the existing literature.⁶

Finally, the work [\[11\]](#page-32-20) on CTL* verification of infinite systems is based on partial symbolic determinization, using prophecy variables to summarize the future program execution. For finite alphabets, automata are a strictly more expressive formalism than temporal logics.^{[7](#page-6-1)} Such a comparison is, however, non-trivial for languages over infinite alphabets. However, in practice, we found the generic register automata considered in this paper to be a natural tool for specifying verification conditions of array programs [\[8](#page-32-5)[,18](#page-32-2)[,19\]](#page-32-3) and regular properties of timed languages [\[2](#page-32-0)].

 $6\,$ Our reduction to the emptiness of product automata is at least exponential.

 $⁷$ For (in)finite words, the class of LTL-definable languages coincides with the star-free languages, which are</sup> a strict subclass of $(\omega$ -)regular languages.

1.3 Organization of the paper

The rest of the paper is organized as follows: Sect. [2](#page-7-0) describes preliminaries. Section [3](#page-8-0) discusses closure properties of the considered class of generic register automata. Section [4](#page-11-0) describes the trace inclusion semi-algorithm. Section [5](#page-21-0) presents a concept of simulation relations on register automata and their integration into the proposed trace inclusion algorithm. Section [6](#page-26-0) is an overview of our experimental evaluation, and, finally, Sect. [7](#page-29-0) concludes the paper.

2 Preliminary definitions

Let N denote the set of non-negative integers including zero. For any $k, \ell \in \mathbb{N}, k \leq \ell$, we write $[k, \ell]$ for the set $\{k, k+1, \ldots, \ell\}$. We write \perp and \top for the boolean constants *false* and *true*, respectively. Given a possibly infinite data domain D , we denote by Form (D) = $\langle \mathcal{D}, f_1, \ldots, f_m \rangle$ the set of syntactically correct first-order formulae with function symbols f_1, \ldots, f_m . A variable *x* is said to be *free* in a formula ϕ , denoted as $\phi(x)$, iff it does not occur under the scope of a quantifier.

Let $\mathbf{x} = \{x_1, \ldots, x_n\}$ be a finite set of variables. A *valuation* $\nu : \mathbf{x} \to \mathcal{D}$ is an assignment of the variables in **x** with values from D . We denote by D^x the set of such valuations. For a formula $\phi(\mathbf{x})$, we denote by $\nu \models \phi$ the fact that substituting in ϕ each variable $x \in \mathbf{x}$ by $v(x)$ yields a valid formula in the first-order theory of Form(*D*). In this case, *v* is said to be a *model* of ϕ . A formula is said to be *satisfiable* iff it has a model. For a formula $\phi(\mathbf{x}, \mathbf{x}')$ where $\mathbf{x}' = \{x' \mid x \in \mathbf{x}\}\$ and two valuations $v, v' \in \mathcal{D}^{\mathbf{x}}$, we denote by $(v, v') \models \phi$ the fact that the formula obtained from ϕ by substituting each *x* with $v(x)$ and each *x'* with $v'(x')$ is valid in the first-order theory of Form(*D*).

2.1 Generic register automata

Generic register automata[8](#page-7-1) (GRA) are extensions of non-deterministic finite automata with variables ranging over an infinite data domain D with the first-order theory of Form (D) . Formally, a GRA is a tuple $A = \langle \mathcal{D}, \Sigma, \mathbf{x}, \mathcal{Q}, \iota, F, \Delta \rangle$, where:

- $-$ Σ is a finite alphabet of input events and \diamond ∈ Σ is a special padding symbol,
- $-\mathbf{x} = \{x_1, \ldots, x_n\}$ is a set of variables,
- $−$ *Q* is a finite set of *states*, *is an <i>initial* state, *are <i>final* states, and
- $-\Delta$ is a set of *rules* of the form $q \xrightarrow{\sigma, \phi(\mathbf{x}, \mathbf{x}')} q'$ where $\sigma \in \Sigma$ is an alphabet symbol and $\phi(\mathbf{x}, \mathbf{x}')$ is a formula in Form (\mathcal{D}) .

A *configuration* of *A* is a pair $(q, v) \in Q \times D^{\mathbf{x}}$. We say that a configuration (q', v') is a *successor* of (q, v) if and only if there exists a rule $q \stackrel{\sigma, \phi}{\longrightarrow} q' \in \Delta$ and $(v, v') \models \phi$. We denote the successor relation by $(q, v) \stackrel{\sigma, \phi}{\longrightarrow} {}_{A}(q', v')$, and we omit writing ϕ and *A* when no confusion may arise. We denote by $succ_A(q, v) = \{(q', v') | \exists \sigma \in \Sigma : (q, v) \stackrel{\sigma}{\rightarrow} A(q', v')\}$ the set of successors of a configuration (q, v) .

For any $n \ge 0$, a *trace* is a finite sequence $w = (v_0, \sigma_0), \ldots, (v_{n-1}, \sigma_{n-1}), (v_n, \diamond)$ of pairs (v_i, σ_i) taken from the infinite alphabet $\mathcal{D}^{\mathbf{x}} \times \Sigma$ [if $n = 0$, the trace is just (v_0, \diamond)]. A *run* of *A* over the *trace* w is a sequence of configurations π : $(q_0, v_0) \stackrel{\sigma_0}{\rightarrow} (q_1, v_1) \stackrel{\sigma_1}{\rightarrow}$

⁸ Called data automata in [\[24](#page-32-4)].

 $\cdots \xrightarrow{\sigma_{n-1}} (q_n, v_n)$ [for $n = 0$, the run is (q_0, v_0) only], where $q_0 = \iota$. We say that the run π is *accepting* if and only if $q_n \in F$, in which case *A accepts* w. The *language* of *A*, denoted $\mathcal{L}(A)$, is the set of traces accepted by *A*.

2.2 Generic register automata networks

A *generic register automata network* (GRAN) is a non-empty tuple $A = \langle A_1, \ldots, A_N \rangle$ of generic register automata $A_i = \langle \mathcal{D}, \Sigma_i, \mathbf{x}_i, Q_i, \iota_i, F_i, \Delta_i \rangle$, $i \in [1, N]$ whose sets of states are pairwise disjoint. A GRAN is a succint representation of an exponentially larger GRA $A^e = \langle \mathcal{D}, \Sigma_{\mathcal{A}}, \mathbf{x}_{\mathcal{A}}, Q_{\mathcal{A}}, \iota_{\mathcal{A}}, F_{\mathcal{A}}, \Delta_{\mathcal{A}} \rangle$, called the *expansion* of *A*, where:

- $-\Sigma_{\mathcal{A}} = \Sigma_{1} \cup ... \cup \Sigma_{N}$ and $\mathbf{x}_{\mathcal{A}} = \mathbf{x}_{1} \cup ... \cup \mathbf{x}_{N}$,
- $-Q_A = Q_1 \times \cdots \times Q_N$, $\iota_A = \langle \iota_1, \ldots, \iota_N \rangle$ and $F_A = F_1 \times \cdots \times F_N$,
- $\langle q_1, \ldots, q_N \rangle \stackrel{\sigma, \varphi}{\longrightarrow} \langle q'_1, \ldots, q'_N \rangle$ if and only if there exists a set of indices $I \subseteq [1, N]$ such that (i) for all $i \in I$, $q_i \stackrel{\sigma, \varphi_i}{\longrightarrow} q'_i \in \Delta_i$, (ii) for all $i \notin I$, $q_i = q'_i$, and (iii) $\varphi \equiv \bigwedge_{i \in I} \varphi_i \wedge \bigwedge_{j \notin I} \tau_j$, where $I = \{i \in [1, N] \mid q_i \stackrel{\sigma, \varphi_i}{\longrightarrow} q'_i \in \Delta_i \text{ for some } \varphi_i\}$ is the set of GRA that can move from q_i to q'_i while reading the input symbol σ , and $\tau_j \equiv \bigwedge_{x \in \mathbf{x}_j \setminus (\bigcup_{j \in J} \mathbf{x}_i)} x' = x$ propagates the values of the local variables in *A_j* that are not updated by ${A_i}_{i \in I}$.

Intuitively, all automata that can read an input symbol synchronize their actions on that symbol whereas the rest of the automata make an idle step and copy the values of their local variables which are not updated by the active automata. The language of the GRAN *A* is defined as the language of its expansion GRA, i.e., $\mathcal{L}(\mathcal{A}) = \mathcal{L}(\mathcal{A}^e)$.

2.3 Trace inclusion

Let *A* be a GRAN and $A^e = \langle \mathcal{D}, \Sigma, \mathbf{x}_A, Q_A, \iota_A, F_A, \Delta_A \rangle$ be its expansion. For a set of variables $y \subseteq x_{\mathcal{A}}$, we denote by $v \downarrow_{v}$ the restriction of a valuation $v \in \mathcal{D}^{x_{\mathcal{A}}}$ to the variables in **y**. For a trace $w = (v_0, \sigma_0), \ldots, (v_n, \diamond) \in (\mathcal{D}^X \times \Sigma_A)^*$, we denote by $w \downarrow_y$ the trace $(v_0\downarrow_y, \sigma_0), \ldots, (v_{n-1}\downarrow_y, \sigma_{n-1}), (v_n\downarrow_y, \diamond) \in (\mathcal{D}^y \times \Sigma)^*$. We lift this notion to sets of words in the natural way, by defining $\mathcal{L}(\mathcal{A})\downarrow_{\mathbf{y}} = \{w\downarrow_{\mathbf{y}} | w \in \mathcal{L}(\mathcal{A})\}.$

We are now ready to define the trace inclusion problem on which we focus in this paper. Given a GRAN *A* as before and a GRA $B = \langle D, \Sigma, \mathbf{x}_B, Q_B, \iota_B, F_B, \Delta_B \rangle$ such that $\mathbf{x}_B \subseteq \mathbf{x}_A$, the *trace inclusion problem* asks whether $\mathcal{L}(\mathcal{A})\downarrow_{\mathbf{X}_B} \subseteq \mathcal{L}(B)$? The right-hand side GRA *B* is called *observer*, and the variables in **x***^B* are called *observable* variables.

3 Boolean closure properties of generic register automata

We show first that generic register automata are closed under the boolean operations of union, intersection and complement and that they are amenable to determinization. Clearly, the emptiness problem is, in general, undecidable, due to the result of Minsky on 2-counter machines with integer variables, increment, decrement and zero test [\[30\]](#page-32-7).

Let $A = \langle \mathcal{D}, \Sigma, \mathbf{x}, \mathcal{Q}, \iota, F, \Delta \rangle$ be a GRA for the rest of this section. *A* is said to be *deterministic* if and only if, for each trace $w \in \mathcal{L}(A)$, *A* has at most one run over w. The first result of this section is that, interestingly, any GRA can be determinized while preserving its language. The determinization procedure is a generalization of the classical subset construction for Rabin-Scott word automata on finite alphabets. The reason why determinization is possible for automata over an infinite data alphabet $D^{\mathbf{x}} \times \Sigma$ is that the successive values taken by *each variable* $x \in \mathbf{x}$ are tracked by the language $\mathcal{L}(A) \subseteq (\mathcal{D}^{\mathbf{x}} \times \Sigma)^*$. This assumption is crucial: a typical example of automata over an infinite alphabet, that cannot be determinized, are timed automata [\[2\]](#page-32-0), where only the elapsed time is reflected in the language, and not the values of the variables (clocks).

Formally, the *deterministic* GRA accepting the language $\mathcal{L}(A)$ is defined as A^d = $(D, \Sigma, \mathbf{x}, Q^d, \iota^d, F^d, \Delta^d)$, where $Q^d = 2^Q, \iota^d = {\iota}, F^d = {P \subseteq Q \mid P \cap F \neq \emptyset}$ and Δ^d is the set of rules $P \stackrel{\sigma, \theta}{\longrightarrow} P'$ such that the formula:

$$
\theta(\mathbf{x}, \mathbf{x}') = \bigwedge_{p' \in P'} \bigvee_{\substack{\sigma, \psi \\ p \in P}} \psi \land \bigwedge_{p' \in Q \setminus P'} \bigwedge_{\substack{\sigma, \varphi \\ p' \in Q \setminus P'}} \neg \varphi
$$

is satisfiable.^{[9](#page-9-0)} The main difference with the classical subset construction for Rabin-Scott automata is that here we consider *all sets* P' of states that have a predecessor in P , not just the maximal such set. This refined subset construction takes into account not just the alphabet symbols in Σ , but also the valuations of the variables in **x**. Observe, moreover, that A^d can be built for any first-order theory of Form(*D*) that is closed under conjunction, disjunction, and negation. The following lemma states the main properties of *A^d* .

Lemma 1 *Given a GRA A* = $\langle \mathcal{D}, \Sigma, \mathbf{x}, Q, \iota, F, \Delta \rangle$, (1) *for any* $w \in (\mathcal{D}^{\mathbf{x}} \times \Sigma)^*$ *and* $P \in Q^d$, *A*^{*d*} *has exactly one run on* w *that starts in P, and (2)* $\mathcal{L}(A) = \mathcal{L}(A^d)$ *.*

Proof (1) Let $w = (v_0, \sigma_0), \ldots, (v_{n-1}, \sigma_{n-1}), (v_n, \diamond)$ be an arbitrary trace and $P \subseteq Q$ be a state of A^d . We first build a run $\pi = (P_0, \nu_0) \xrightarrow{\sigma_0, \theta_0} (P_1, \nu_1) \cdots \xrightarrow{\sigma_{n-1}, \theta_{n-1}} (P_n, \nu_n)$ of A^d such that $P_0 = P$, by induction on $n \ge 0$. If $n = 0$, then $w = (v_0, \diamond)$ and $\pi = (P_0, v_0)$ is trivially a run of A^d over w. For the induction step, let $n > 0$ and suppose that A^d has a run $(P_0, \nu_0) \xrightarrow{\sigma_0, \theta_0} \cdots (P_{n-1}, \nu_{n-1})$ such that $P_0 = P$. We extend this run to a run over w by considering:

$$
P_n = \left\{ p \in Q \mid \exists q \in P_{n-1} \, . \, q \xrightarrow{\sigma_{n-1}, \phi} p \in \Delta \text{ and } (\nu_{n-1}, \nu_n) \models \phi \right\},\
$$

$$
\theta_n \equiv \bigwedge_{p' \in P_n} \bigvee_{p \xrightarrow{\sigma, \psi} p' \in \Delta} \psi \wedge \bigwedge_{p' \in Q \setminus P_n} \bigwedge_{p \xrightarrow{\sigma, \phi} p' \in \Delta} \neg \varphi.
$$

It is not hard to see that $(v_{n-1}, v_n) \models \theta_n$, thus $(P_0, v_0) \xrightarrow{\sigma_0, \theta_0} \cdots \xrightarrow{\sigma_n, \theta_n} (P_n, v_n)$ is indeed a run of A^d over w. To show that π is unique, suppose, by contradiction, that there exists a different run $\pi' = (R_0, v_0) \xrightarrow{\sigma_0, \omega_0} (R_1, v_1) \cdots \xrightarrow{\sigma_{n-1}, \omega_{n-1}} (R_n, v_n)$ such that $P_0 = R_0 = P$. Notice that the relation labeling any transition rule $P_i \xrightarrow{\sigma_i, \theta_i} P_{i+1}$ is entirely determined by the sets P_i and P_{i+1} , so two runs are different iff they differ in at least one state, i.e., $P_i \neq R_j$ for some $j \in [1, n]$. Let *i* denote the smallest such *j* and suppose that there exists $p \in P_i$ such that $p \notin R_i$ (the symmetrical case $p \in R_i$ and $p \notin P_i$ is left to the reader). By the definition of Δ^d , there exists $q \in P_{i-1} = R_{i-1}$ such that $q \xrightarrow{i} p \in \Delta$. Since $(v_{i-1}, v_i) \models \theta_{i-1} \land \omega_{i-1}$, we obtain that $(v_{i-1}, v_i) \models \bigvee \{ \psi \mid q \xrightarrow{\sigma_{i-1}, \psi} p \in \Delta, q \in P_{i-1} \}$

⁹ Note that the empty disjunction is equivalent to \perp . Hence θ (**x**, **x**[']) satisfiable implies that for all $p' \in P'$ there exists $p \in P$ and a rule $p \xrightarrow{\sigma, \psi} p' \in \Delta$.

and $(v_{i-1}, v_i) \models \bigwedge \{\neg \psi \mid q \xrightarrow{\sigma_{i-1}, \psi} p \in \Delta, q \in P_{i-1}\},$ contradiction. Thus π is the only run of A^d over w starting in *P*.

(2) Let $w = (v_0, \sigma_0), \ldots, (v_{n-1}, \sigma_{n-1}), (v_n, \diamond)$ be a trace. " \subseteq " If $w \in \mathcal{L}(A)$, then A has a run $(q_0, v_0) \xrightarrow{\sigma_0, \phi_0} \cdots \xrightarrow{\sigma_{n-1}, \phi_{n-1}} (q_n, v_n)$ such that $q_0 = \iota$ and $q_n \in F$. By Point 1, A^d has a unique run $(P_0, v_0) \xrightarrow{\sigma_0, \theta_0} \cdots \xrightarrow{\sigma_{n-1}, \theta_{n-1}} (P_n, v_n)$ over w. We prove that $q_i \in P_i$ by induction on $i \in [0, n]$. For $i = 0$, we have $P_0 = \{i\}$ by the definition of A^d . For the induction step, suppose that $i \in [1, n]$ and $q_{i-1} \in P_{i-1}$. By contradiction, assume that $q_i \notin P_i$. Since $(v_{i-1}, v_i) \models \theta_{i-1}$, we obtain $(v_{i-1}, v_i) \models \neg \phi_{i-1}$, contradiction. Thus $q_i \in P_i$ for all $i \in [0, n]$, and $q_n \in P_n$, hence $P_n \cap F \neq \emptyset$. Then $P_n \in F^d$, and $w \in \mathcal{L}(A^d)$. " \supseteq " If $w \in \mathcal{L}(A^d)$, then A^d has a (unique) run $(P_0, v_0) \xrightarrow{\sigma_0, \theta_0} (P_1, v_1) \cdots \xrightarrow{\sigma_{n-1}, \theta_{n-1}} (P_n, v_n)$ over w such that $P_0 = \{i\}$ and $P_n \cap F \neq \emptyset$. Then there exists $p_n \in P_n \cap F$, and, by the definition of A^d , there exists $p_{n-1} \in P_{n-1}$ such that $p_{n-1} \xrightarrow{\sigma_{n-1}, \psi_{n-1}} p_n \in \Delta$ and $(\psi_{n-1}, \psi_n) \models \psi_{n-1}$. Continuing this argument backwards, we can find a run $(q_0, v_0) \xrightarrow{\sigma_0, \psi_0} \cdots \xrightarrow{\sigma_{n-1}, \psi_{n-1}} (q_n, v_n)$ of *A* over w such that $q_i \in P_i$ for all $i \in [0, n]$. Since $P_0 = \{i\}$ and $q_n \in F$, we obtain that $w \in \mathcal{L}(A)$. $w \in \mathcal{L}(A).$

The construction of a deterministic GRA recognizing the language of *A* is key to defining a GRA that recognizes the complement of *A*. Let $\overline{A} = \langle \mathcal{D}, \Sigma, \mathbf{x}, \mathcal{Q}^d, \iota^d, \mathcal{Q}^d \setminus F^d, \Delta^d \rangle$. In other words, \overline{A} has the same structure as A^d , and the set of final states consists of those subsets that contain no final state, i.e., $\{P \subseteq Q \mid P \cap F = \emptyset\}$. Using Lemma [1,](#page-9-1) it is not difficult to show that $\mathcal{L}(\overline{A}) = (\mathcal{D}^{\mathbf{x}} \times \Sigma)^* \setminus \mathcal{L}(A)$.

Next, we show closure of GRA under intersection. Let $B = \langle D, \Sigma, \mathbf{x}, Q', \iota', F', \Delta' \rangle$ be a GRA and define $A \times B = \langle \mathcal{D}, \Sigma, \mathbf{x}, \mathcal{Q} \times \mathcal{Q}', (i, i'), F \times F', \Delta^{\times} \rangle$ where $(q, q') \stackrel{\sigma, \varphi}{\longrightarrow}$ $(p, p') \in \Delta^{\times}$ if and only if $q \stackrel{\sigma, \phi}{\longrightarrow} p \in \Delta$, $q' \stackrel{\sigma, \psi}{\longrightarrow} p' \in \Delta'$ and $\varphi \equiv \phi \wedge \psi$. It is easy to show that $\mathcal{L}(A \times B) = \mathcal{L}(A) \cap \mathcal{L}(B)$. GRA are also closed under union since $\mathcal{L}(A) \cup \mathcal{L}(B) = \mathcal{L}(\overline{A} \times \overline{B}).$

Let us turn now to the trace inclusion problem. The following lemma shows that this problem can be effectively reduced to an equivalent language emptiness problem. However, note that this reduction does not work when the trace inclusion problem is generalized by removing the condition $\mathbf{x}_B \subseteq \mathbf{x}_A$. In other words, if the observer uses local variables not shared with the network,¹⁰ i.e., $\mathbf{x}_B \setminus \mathbf{x}_A \neq \emptyset$, the generalized trace inclusion problem $\mathcal{L}(A) \downarrow_{\mathbf{x}_A \cap \mathbf{x}_B} \subseteq \mathcal{L}(B) \downarrow_{\mathbf{x}_A \cap \mathbf{x}_B}$ has a negative answer iff *there exists a trace* $w = (v_0, \sigma_0), \ldots, (v_n, \diamond) \in \mathcal{L}(A)$ such that, *for all valuations* $\mu_0, \ldots, \mu_n \in \mathcal{D}^{\mathbf{x}_B \setminus \mathbf{x}_A}$, we have $w' = (v_0 \downarrow_{\mathbf{x}_A \cap \mathbf{x}_B} \cup \mu_0, \sigma_0), \dots, (v_n \downarrow_{\mathbf{x}_A \cap \mathbf{x}_B} \cup \mu_n, \diamond) \notin \mathcal{L}(B)$. This kind of quantifier alternation cannot be easily accommodated within the framework of language emptiness, in which only one type of (existential) quantifier occurs.

Lemma 2 $Given$ GRA A $=$ $\langle \mathcal{D}, \Sigma, \mathbf{x}_A, Q_A, \iota_A, F_A, \Delta_A \rangle$ and $B =$ $\langle \mathcal{D}, \Sigma, \mathbf{x}_B, Q_B, \iota_B, F_B, \Delta_B \rangle$ such that $\mathbf{x}_B \subseteq \mathbf{x}_A$. Then $\mathcal{L}(A) \downarrow_{\mathbf{x}_B} \subseteq \mathcal{L}(B)$ *if and only if* $\mathcal{L}(A \times \overline{B}) = \emptyset$.

Proof We have
$$
\mathcal{L}(A) \downarrow_{\mathbf{x}_B} \subseteq \mathcal{L}(B)
$$
 iff $\mathcal{L}(A) \downarrow_{\mathbf{x}_B} \cap \mathcal{L}(\overline{B}) = \mathcal{L}(A \times \overline{B}) \downarrow_{\mathbf{x}_B} = \emptyset$ iff $\mathcal{L}(A \times \overline{B}) = \emptyset$.

The trace inclusion problem is undecidable, which can be shown by reduction from the language emptiness problem for GRA (take *B* such that $\mathcal{L}(B) = \emptyset$). However, the above

¹⁰ For timed automata, this is the case since the only shared variable is the time, and the observer may have local clocks.

lemma shows that any semi-decision procedure for the language emptiness problem can also be used to deal with the trace inclusion problem.

4 Abstract, check, and refine for trace inclusion

This section describes our semi-algorithm for checking the trace inclusion between a given network *^A* and an observer *^B*. Let *^A^e* denote the expansion of *^A*, defined in Sect. [2.](#page-7-0) In the light of Lemma [2,](#page-10-1) the trace inclusion problem $\mathcal{L}(\mathcal{A})\downarrow_{X_B} \subseteq \mathcal{L}(B)$, where the set of observable variables x_B is included in the set of network variables, can be reduced to the language emptiness problem $\mathcal{L}(\mathcal{A}^e \times \overline{B}) = \emptyset$.

Although language emptiness is in general undecidable for generic register automata [\[30\]](#page-32-7), several cost-effective semi-algorithms and tools [\[3](#page-32-8)[,17](#page-32-9)[,21](#page-32-22)[,27\]](#page-32-11) have been developed, showing that it is possible, in many practical cases, to provide a yes/no answer to this problem. However, to apply one of the existing off-the-shelf tools to our problem, one needs to build the product automaton $A^e \times \overline{B}$ prior to the analysis. Due to the inherent state explosion caused by the interleaving semantics of the network as well as by the complementation of the observer, such a solution would not be efficient in practice.

To avoid building the product automaton, our procedure builds *on-the-fly* an overapproximation of the (possibly infinite) set of reachable configurations of $A^e \times B$. This over-approximation is defined using the approach of *lazy predicate abstraction* [\[21](#page-32-22)], combined with *counterexample-driven abstraction refinement* using *interpolants* [\[27\]](#page-32-11). We store the explored abstract states in a structure called an *antichain tree*. In general, antichain-based algorithms [\[1](#page-32-12)[,35](#page-33-1)] store only states which are incomparable wrt a partial order called *subsumption*. Our method can be thus seen as an extension of the antichain-based language inclusion algorithms [\[1](#page-32-12)[,35\]](#page-33-1) to infinite state systems by means of predicate abstraction and interpolation-based refinement. Since the trace inclusion problem is undecidable in general, termination of our procedure is not guaranteed; in the following, we shall, however, call our procedure an algorithm for the sake of brevity.

4.1 Antichain trees

In this section, we define antichain trees, which are the main data structure of the trace inclusion (semi-)algorithm. Let $A = \langle A_1, \ldots, A_N \rangle$ be a network of automata where A_i $\langle \mathcal{D}, \Sigma_i, \mathbf{x}_i, Q_i, \iota_i, F_i, \Delta_i \rangle$, for all $i \in [1, N]$, and let $B = \langle \mathcal{D}, \Sigma, \mathbf{x}_B, Q_B, \iota_B, F_B, \Delta_B \rangle$ be an observer such that $\mathbf{x}_B \subseteq \bigcup_{i=1}^N \mathbf{x}_i$. We also denote by $\mathcal{A}^e = \langle \mathcal{D}, \Sigma_{\mathcal{A}}, \mathbf{x}_A, Q_{\mathcal{A}}, \iota_A, F_{\mathcal{A}}, \Delta_{\mathcal{A}} \rangle$ the expansion of the network *A* and by $A^e \times \overline{B} = \langle D, \Sigma_A, x_A, Q^p, t^p, F^p, \Delta^p \rangle$ the product automaton used for checking language inclusion.

An *antichain tree* for the network *A* and the observer *B* is a tree whose nodes are labeled by *product states* (see Fig. [2](#page-4-0) for examples).¹¹ Intuitively, a product state is an over-approximation of the set of reachable configurations of the product automaton $A^e \times \overline{B}$ that share the same control state. Formally, a *product state for A and B* is defined as a tuple $s = (\mathbf{q}, P, \Phi)$ where (i) (**q**, *P*) is a state of $A^e \times B$ with $\mathbf{q} = \langle q_1, \ldots, q_N \rangle$ being a state of the network expansion A^e and *P* being a set of states of the observer *B*, and (ii) $\Phi(\mathbf{x}_A) \in \text{Form}(\mathcal{D})$ is a formula which defines an over-approximation of the set of valuations of the variables **x***^A*

¹¹ The formal definition of antichain trees will be given as Definition [1](#page-13-0) later in this section.

that reach the state (q, P) in $A^e \times \overline{B}$. A product state $s = (q, P, \Phi)$ is a finite representation of a possibly infinite set of configurations of $\mathcal{A}^e \times \overline{B}$, denoted as $[\![s]\!] = \{(\mathbf{q}, P, v) \mid v \models \Phi\}.$ ^{[12](#page-12-0)}

To build an over-approximation of the set of reachable states of the product automaton, we need to compute, for a product state *s*, an over-approximation of the set of configurations that can be reached in one step from *s*. To this end, we define first a finite abstract domain of product states, based on the notion of *predicate map*. A predicate map is a partial function that associates sets of facts¹³ about the values of the variables used in the product automaton with components of a product state. Facts are called *predicates* and components of a product state are called *substates*. Formally, a *substate* of a state $(\langle q_1, \ldots, q_N \rangle, P) \in Q^p$ of the product automaton $A^e \times \overline{B}$ is a pair $(\langle q_{i_1}, \ldots, q_{i_k} \rangle, S)$ such that (i) $\langle q_{i_1}, \ldots, q_{i_k} \rangle$ is a subsequence of $\langle q_1, \ldots, q_N \rangle$, and (ii) *S* $\neq \emptyset$ only if *S* \cap *P* $\neq \emptyset$.

The reason behind the distribution of predicates over substates is two-fold. First, we would like the abstraction to be *local*, i.e., the predicates needed to define a certain subtree in the antichain must be associated with the labels of that subtree only. Second, once a predicate appears in the context of a substate, it should be subsequently reused whenever that same substate occurs as part of another product state.

We denote the substate relation by $(\langle q_{i_1}, \ldots, q_{i_k} \rangle, S) \lhd (\langle q_1, \ldots, q_N \rangle, P)$. The substate relation requires the automata A_{i_1}, \ldots, A_{i_k} of the network *A* to be in the control states q_i_1, \ldots, q_i_k simultaneously, and the observer *B* to be in at least some state of *S* provided $S \neq \emptyset$ (if *S* = \emptyset , the state of *B* is considered to be irrelevant). Let $\mathcal{S}_{\langle A,B\rangle} = \{r \mid \exists q \in Q^p$. $r \lhd q\}$ be the set of substates of a state of $A^e \times \overline{B}$.

A *predicate map* $\Pi : S_{\langle A,B \rangle} \to 2^{\text{Form}(\mathcal{D})}$ associates each substate $(\mathbf{r}, S) \in Q_{i_1} \times ... \times$ $Q_{i_k} \times 2^{\mathcal{Q}_B}$ with a set of formulae $\pi(\mathbf{x})$ where (i) $\mathbf{x} = \mathbf{x}_{i_1} \cup \ldots \cup \mathbf{x}_{i_k} \cup \mathbf{x}_B$ if $S \neq \emptyset$, and (ii) **x** = **x**_{*i*1}</sub> ∪ ... ∪ **x**_{*ik*} if *S* = \emptyset .

Notice that a predicate associated with a substate refers only to the local variables of those network components A_{i_1}, \ldots, A_{i_k} and of the observer *B* that occur in the particular substate.

Example 1 The antichain in Fig. [2d](#page-4-0) uses the predicate map $(\langle q_1^1, q_1^2 \rangle, \{p_1\}) \mapsto \{v = 1\},\$ $(\langle q_1^1, q_1^2 \rangle, \{p_2\}) \mapsto {\Delta < x, v = 2}.$

We are now ready to define the abstract semantics of the product automaton $A^e \times \overline{B}$, induced by a given predicate map. For convenience, we define first a set *Post*(*s*) of *concrete successors* of a product state $s = (\mathbf{q}, P, \Phi)$ such that $(\mathbf{r}, S, \Psi) \in Post(s)$ if and only if (i) the product automaton $\mathcal{A}^e \times \overline{B}$ has a rule $(\mathbf{q}, P) \xrightarrow{\sigma, \theta} (\mathbf{r}, S) \in \Delta^p$ and $\Psi(\mathbf{x}_\mathcal{A}) \equiv$ \exists **x**[']_A. Φ (**x**[']_A) \land θ (**x**[']_A, **x**_A) is satisfiable.^{[14](#page-12-2)}

Given a predicate map Π , the set *Post* $\Pi(s)$ of *abstract successors* of a product state *s* is defined as follows: $(\mathbf{r}, S, \Psi^{\sharp}) \in Post_{\Pi}(s)$ if and only if (i) there exists a product state $(\mathbf{r}, S, \Psi) \in Post(s)$ and (ii) $\Psi^{\sharp}(\mathbf{x}_{\mathcal{A}}) \equiv \bigwedge_{r \leq (\mathbf{r}, S)} \bigwedge {\pi \in \Pi(r) \mid \Psi \rightarrow \pi}.$ In other words, the set of data valuations reachable by an abstract successor is the tightest over-approximation

¹² Note that the above choice of the product state in the form $s = (\mathbf{q}, P, \Phi)$ is not straightforward and resulted from several previous unsuccessful attempts. For example, if one chooses to associate separate formulae for the valuations of the variables with **q** and each of the states in P , which seems to be a quite natural choice, the construction becomes unsound. Intuitively, when a successor state of such a product state is computed, the disjunction of the formulae joint with the successors of *P* may entail the formula joint with the successor of **^q**. However, that does not mean that all pairs of source/target valuations possible in *^A^e* are possible in *^B* too. More details are provided in "Appendix [1"](#page-31-0).

¹³ A fact is a formula in Form (D) .

¹⁴ If θ (**x**['] A </sup>, **x**_{A}) is unsatisfiable, then *s* does not contain a valuation of the variables that would allow one to do a step following the rule $(\mathbf{q}, P) \xrightarrow{\sigma, \theta} (\mathbf{r}, S)$.

of the concrete set of reachable valuations, obtained as the conjunction of the available predicates from the predicate map that over-approximate this set.

Example 2 (*Continued from Example* [1\)](#page-12-3) Consider the antichain from Fig. [2d](#page-4-0). The concrete successors of $s = (\langle q_1^1, q_1^2 \rangle, \{p_1\}, v = 1)$ are $(\langle q_1^1, q_1^2 \rangle, \{p_1\}, \Psi_1)$ and $(\langle q_1^1, q_1^2 \rangle, \{p_2\}, \Psi_2)$:

 $\Psi_1 \equiv \exists v', x', \Delta', v' = 1 \land x = x' + 1 \land v = 1 \land \Delta = \Delta' \land 0 \le x' < \Delta \land v = v',$ $\Psi_2 \equiv \exists v', x', \Delta', v' = 1 \land x = x' + 1 \land v = 2 \land \Delta = \Delta' \land \Delta \le x' < 2\Delta \land v = v' + 1.$

With predicate map Π from Example [1,](#page-12-3)

$$
Post_{\Pi}(s) = \{ (\langle q_1^1, q_1^2 \rangle, \{p_1\}, \Psi_1^{\sharp}), (\langle q_1^1, q_1^2 \rangle, \{p_2\}, \Psi_2^{\sharp}) \} : (\Psi_1 \to v = 1) \qquad \to (\Psi_1^{\sharp} \equiv v = 1), (\Psi_2 \to v = 2 \text{ and } \Psi_2 \to \Delta < x) \to (\Psi_2^{\sharp} \equiv v = 2 \land \Delta < x).
$$

Finally, an *antichain tree* (or, simply antichain) T for A and B is a tree whose nodes are labeled with product states and whose edges are labeled by input symbols and concrete transition relations. Let N[∗] be the set of finite sequences of natural numbers that denote the positions in the tree. For a tree position $p \in \mathbb{N}^*$ and $i \in \mathbb{N}$, the position *p.i* is a *child* of *p*. A set *S* ⊆ \mathbb{N}^* is said to be *prefix-closed* if and only if, for each *p* ∈ *S* and each prefix *q* of *p*, we have $q \in S$ as well. The root of the tree is denoted by the empty sequence ε .

Definition 1 Formally, an antichain T is a set of pairs $\langle s, p \rangle$, where *s* is a product state and $p \in \mathbb{N}^*$ is a tree position, such that (1) (2) for each position $p \in \mathbb{N}^*$ there exists at most one product state *s* such that $\langle s, p \rangle \in \mathcal{T}$, (3) the set $\{p \mid \langle s, p \rangle \in \mathcal{T}\}\$ is prefix-closed, (4) $(root(A, B), \varepsilon) \in \mathcal{T}$ where $root(A, B) = ((\iota_1, \ldots, \iota_N), \{\iota_B\}, \top)$ is the label of the root, and (5) for each edge ($\langle s, p \rangle$, $\langle t, p.i \rangle$) in *T*, there exists a predicate map Π such that $t \in Post_{\Pi}(s)$. For the latter condition, if $s = (\mathbf{q}, P, \Phi)$ and $t = (\mathbf{r}, S, \Psi)$, there exists a unique rule $(\mathbf{q}, P) \xrightarrow{\sigma, \theta} (\mathbf{r}, S) \in \Delta^p$, and we shall sometimes denote the edge as $s \xrightarrow{\sigma, \theta} t$ or simply $s \xrightarrow{\theta} t$ when the tree positions or alphabet symbols are not important.

Each antichain node $n = (s, d_1 \dots d_k) \in \mathcal{T}$ is naturally associated with a path from the root to itself $\rho: n_0 \xrightarrow{\sigma_1, \theta_1} n_1 \xrightarrow{\sigma_2, \theta_2} \cdots \xrightarrow{\sigma_k, \theta_k} n_k$. We denote by ρ_i the node n_i for each $i \in [0, k]$, and by $|\rho| = k$ the length of the path. The *path formula* associated with ρ is $\Theta(\rho) \equiv \bigwedge_{i=1}^{k} \theta_i(\mathbf{x}_{\mathcal{A}}^{i-1}, \mathbf{x}_{\mathcal{A}}^i)$ where $\mathbf{x}_{\mathcal{A}}^i = \{x^i \mid x \in \mathbf{x}_{\mathcal{A}}\}$ is a set of indexed variables for each $i ∈ [0, k]$.

Example 3 Consider the following path ρ : $(\langle q_0^1, q_0^2 \rangle, \{p_0\}, \top) \stackrel{\text{init}}{\longrightarrow} (\langle q_1^1, q_1^2 \rangle, \{p_1\}, v =$ 1) $\stackrel{a_2}{\rightarrow}$ $(\langle q_1^1, q_1^2 \rangle, \{p_2\}, \Delta < x) \stackrel{a_2}{\rightarrow} (\langle q_1^1, q_1^2 \rangle, \emptyset, \Delta < x)$ in the antichain from Fig. [2c](#page-4-0). The path formula of ρ is $\Theta(\rho) \equiv \theta_1 \wedge \theta_2 \wedge \theta_3$ where:

 $\theta_1 \equiv v^1 = 1 \wedge x^1 = 0 \wedge 0 < \Delta^1$, $\theta_2 \equiv v^2 = v^1 + 1 \wedge \Delta^2 = \Delta^1 \wedge v^2 = 2 \wedge x^2 = x^1 + 1 \wedge \Delta^1 \le x^1 < 2\Delta^1 \wedge \neg (v^2 = v^1),$ $\theta_3 \equiv v^3 = 2 \wedge \Delta^3 = \Delta^2 \wedge x^3 = x^2 + 1 \wedge \Delta_2 \le x^2 < 2\Delta^2 \wedge \neg(v^3 = v^2).$

 \Box

4.2 Counterexample-driven abstraction refinement

A *counterexample* is a path from the root of the antichain to a node which is labeled by an *accepting* product state. A product state (q, P, Φ) is said to be *accepting* iff (q, P) is an accepting state of the product automaton $A^e \times \overline{B}$, i.e., $\mathbf{q} \in F_A$ and $P \cap F_B = \emptyset$. A counterexample is said to be *spurious* if its path formula is unsatisfiable, i.e., the path does not correspond to a concrete execution of $A^e \times B$. In this case, we need to (i) remove the path ρ from the current antichain and (ii) refine the abstract domain in order to exclude the occurrence of ρ from future state space exploration.

Let $\rho : root_{\langle A,B \rangle} = (\mathbf{q}_0, P_0, \Phi_0) \stackrel{\theta_1}{\rightarrow} (\mathbf{q}_1, P_1, \Phi_1) \stackrel{\theta_2}{\rightarrow} \cdots \stackrel{\theta_k}{\rightarrow} (\mathbf{q}_k, P_k, \Phi_k)$ be a spurious counterexample in the following. For efficiency reasons, we would like to save as much work as possible and remove only the smallest suffix of ρ which caused the spuriousness. For some $j \in [0, k]$, let $\Theta^j(\rho) \equiv \Phi_j(\mathbf{x}_{\mathcal{A}}^0) \wedge \bigwedge_{i=j}^k \theta_i(\mathbf{x}_{\mathcal{A}}^{i-j}, \mathbf{x}_{\mathcal{A}}^{i-j+1})$ be the formula defining all sequences of data valuations that start in the set Φ_j and proceed along the suffix $({\bf q}_i, P_i, \Phi_i) \rightarrow \cdots \rightarrow ({\bf q}_k, P_k, \Phi_k)$ of ρ . The *pivot* of a path ρ is the maximal position *j* ∈ [0, *k*] such that Θ ^{*j*}(ρ) is unsatisfiable, and −1 if ρ is not spurious.

Example 4 (*Continued from Example* [3\)](#page-13-1) The path formula $\Theta(\rho) \equiv \theta_1 \wedge \theta_2 \wedge \theta_3$ from Exam-ple [3](#page-13-1) is unsatisfiable, thus ρ is a spurious counterexample. Moreover, we have unsatisfiable $\widehat{\Theta}^1(\rho) \equiv \top \wedge \theta_2 \wedge \theta_3$ because of the unsatisfiable subformula $v^2 = 2 \wedge v^3 = 2 \wedge \neg (v^3 = v^2)$. Since $\Theta^2(\rho)$ is satisfiable, the pivot of ρ is 1.

Finally, we describe the refinement of the predicate map, which ensures that a given spurious counterexample will never be found in a future iteration of the abstract state space exploration. The refinement is based on the notion of *interpolant* [\[27\]](#page-32-11).

Definition 2 Given a formula $\Phi(\mathbf{x})$ and a sequence $\langle \theta_1(\mathbf{x}, \mathbf{x}')$, ..., $\theta_k(\mathbf{x}, \mathbf{x}')\rangle$ of formulae, an *interpolant* is a sequence of formulae $I = \langle I_0(\mathbf{x}), \dots, I_k(\mathbf{x}) \rangle$ where: (1) $\Phi \to I_0$, (2) $I_k \to \bot$, and (3) $I_{i-1}(\mathbf{x}) \wedge \theta_i(\mathbf{x}, \mathbf{x}') \to I_i(\mathbf{x}')$ for all $i \in [1, k]$.

Any given interpolant is a witness for the unsatisfiability of a (suffix) path formula $\Theta^j(\rho)$. Dually, if *Craig's Interpolation Lemma* [\[12\]](#page-32-23) holds for the considered first-order data theory, any infeasible path formula is guaranteed to have an interpolant. The interpolant can be computed by means of Satisfiability Modulo Theories (SMT) solvers [\[9](#page-32-24)[,28](#page-32-25)].

Example 5 (*Continued from Example* [4\)](#page-14-0) Let $\Phi \equiv \top$ (variables initially unconstrained) and the sequence of formula be $\langle \theta_1, \theta_2, \theta_3 \rangle$ from Example [3.](#page-13-1) An interpolant is a sequence $I = \langle T, v = 2, \perp \rangle$. $\langle \top, v = 2, \bot \rangle$.

Given a spurious counterexample $\rho = (\mathbf{q}_0, P_0, \Phi_0) \stackrel{\theta_1}{\rightarrow} \cdots \stackrel{\theta_k}{\rightarrow} (\mathbf{q}_k, P_k, \Phi_k)$ with pivot *j* ≥ 0, an interpolant **I** = $\langle I_0, \ldots, I_{k-j} \rangle$ for the infeasible path formula $\Theta^j(\rho)$ can be used to refine the abstract domain by augmenting the predicate map Π . A simple possible refinement is to add the formula I_i into $\Pi((\mathbf{q}_{i+i}, P_{i+i}))$ for each $0 \leq i \leq (k - j)$. As an effect of this refinement, the antichain construction algorithm will avoid every path with the suffix $({\bf q}_i, P_i, \Phi_i) \rightarrow \cdots \rightarrow ({\bf q}_k, P_k, \Phi_k)$ in a future iteration.

We use an improved version of this simple refinement in order to obtain more reusable predicates. If $I_i = C_i^1(\mathbf{y}_1) \wedge \cdots \wedge C_i^{m_i}(\mathbf{y}_{m_i})$ is a conjunctive normal form (CNF) of the *i*th component of the interpolant, we consider the substate $(\mathbf{r}_i^{\ell}, S_i^{\ell})$ for each $C_i^{\ell}(\mathbf{y}_{\ell})$ where $\ell \in [1, m_i]$:

- $-\mathbf{r}_i^{\ell} = \langle q_{i_1}, \ldots, q_{i_h} \rangle$ where $1 \leq i_1 < \cdots < i_h \leq N$ is the largest sequence of indices such that $\mathbf{x}_{i} \cap \mathbf{y}_{\ell} \neq \emptyset$ for each $g \in [1, h]$ and the set \mathbf{x}_{i} of variables of the network component GRA *Aig* ,
- S_i ^{*ℓ*} = *P_i* if **x**_{*B*} ∩ **y**_{*ℓ*} \neq Ø, and S_i ^{*ℓ*} = Ø, otherwise.

A predicate map Π is said to be *compatible* with a spurious path $\rho : s_0 \stackrel{\theta_1}{\rightarrow} \cdots \stackrel{\theta_k}{\rightarrow} s_k$ with pivot *j* ≥ 0 if $s_j = (\mathbf{q}_j, P_j, \Phi_j)$ and there is an interpolant **I** = $\langle I_0, \ldots, I_{k-j} \rangle$ of the suffix $\langle \theta_i, \ldots, \theta_k \rangle$ wrt. Φ_j such that, for each clause *C* of some equivalent CNF of *I_i*, $i \in [0, k-j]$, it holds that $C \in \Pi(r)$ for some substate $r \triangleleft s_{i+j}$. The following lemma proves that, under a predicate map compatible with a spurious path ρ , the antichain construction will exclude further paths that share the suffix of ρ starting with its pivot.

Lemma 3 *Let* ρ : $(\mathbf{q}_0, P_0, \Phi_0) \stackrel{\theta_0}{\rightarrow} (\mathbf{q}_1, P_1, \Phi_1) \stackrel{\theta_1}{\rightarrow} \cdots \stackrel{\theta_{k-1}}{\rightarrow} (\mathbf{q}_k, P_k, \Phi_k)$ be a spurious *counterexample and a predicate map compatible with* ρ*. Then, there is no sequence of product states* $(\mathbf{q}_i, P_i, \Psi_0), \ldots, (\mathbf{q}_k, P_k, \Psi_{k-j})$ *such that:* (1) $\Psi_0 \rightarrow \Phi_i$ *and* (2) $(\mathbf{q}_{i+1}, P_{i+1}, \Psi_{i-j+1}) \in Post_{\Pi}((\mathbf{q}_i, P_i, \Psi_{i-j}))$ *for all i* ∈ [*j*, *k* − 1]*.*

Proof Let $j \in [0, k]$ be the pivot of ρ . Since ρ is spurious, there exists an interpolant $I = \langle I_0, \ldots, I_{k-j} \rangle$ for Φ_j and $\langle \theta_j, \ldots, \theta_k \rangle$. It is sufficient to prove that $\Psi_i \to I_i$ for all $i \in [0, k - j]$. Since $I_{k-j} = \bot$, we obtain $\Psi_{k-j} = \bot$, and consequently $(\mathbf{q}_{k-j}, P_{k-j}, \perp) \in Post_{\Pi}((\mathbf{q}_{k-j-1}, P_{k-j-1}, \Psi_{k-j-1}))$. By the definition of *Post*_{Π}, we have $(\mathbf{q}_{k-j}, P_{k-j}, \perp)$ ∈ *Post*($(\mathbf{q}_{k-j-1}, P_{k-j-1}, \Psi_{k-j-1})$), which contradicts with the definition of *Post*. We show that $\Psi_i \to I_i$ for all $i \in [0, k - j]$, by induction on $k - j$. For the base case $k - j = 0$, we have $\Psi_0 \to \Phi_j \to I_0$. For the induction step, we assume $\Psi_i \to I_i$ for all *i* ∈ [0, *k* − *j* − 1] and prove Ψ_{k-i} → *I_{k−j}*. By the induction hypothesis, we have:

$$
\Psi_{k-j-1}(\mathbf{x}_{\mathcal{A}}) \to I_{k-j-1}(\mathbf{x}_{\mathcal{A}}) \text{ and}
$$

$$
\Psi_{k-j-1}(\mathbf{x}_{\mathcal{A}}) \wedge \theta_{k-j-1}(\mathbf{x}_{\mathcal{A}}, \mathbf{x}'_{\mathcal{A}}) \to I_{k-j-1}(\mathbf{x}_{\mathcal{A}}) \wedge \theta_{k-j-1}(\mathbf{x}_{\mathcal{A}}, \mathbf{x}'_{\mathcal{A}}) \to I_{k-j}(\mathbf{x}'_{\mathcal{A}}).
$$

Let $C_1 \wedge \cdots \wedge C_\ell$ be the CNF of I_{k-j} . Since Π is compatible with ρ , for each clause C_i , there exists a substate $r \lhd (\mathbf{q}_k, P_k)$ such that $C_i \in \Pi(r)$. By the definition of $Post_{\Pi}$, we obtain that Ψ_{k-i} → C_i for each $i \in [1, \ell]$, hence Ψ_{k-i} → I_{k-i} .

Observe that the refinement induced by interpolation is *local* since Π associates sets of predicates with substates of the states in $\mathcal{A}^e \times \overline{B}$, and the update impacts only the states occurring within the suffix of that particular spurious counterexample.

4.3 Subsumption

The main optimization of antichain-based algorithms [\[1](#page-32-12)] for checking language inclusion of automata over finite alphabets is that product states that are *subsets* of already visited states are never stored in the antichain. On the other hand, language emptiness semi-algorithms, based on *predicate abstraction* [\[27\]](#page-32-11) use a similar notion to cover newly generated abstract successor states by those that were visited sooner and that represent larger sets of configurations. In this case, state coverage does not only increase efficiency but also ensures termination of the semi-algorithm in many practical cases.

In this section, we generalize the subset relation used in classical antichain algorithms with the notion of coverage from predicate abstraction, and we define a more general notion of *subsumption* for generic register automata. Given a state (**q**, *P*) of the product automaton $A^e \times \overline{B}$ and a valuation $v \in D^X A$, the *residual language* $\mathcal{L}_{(q,P,\nu)}(\mathcal{A}^e \times \overline{B})$ is the set of traces w accepted by $A^e \times \overline{B}$ from the state (q, P) such that v is the first valuation which occurs on w. This notion is then lifted to a product state $s = (\mathbf{q}, P, \Phi)$ as follows: $\mathcal{L}_s(\mathcal{A}^e \times$ \overline{B}) = $\bigcup_{(\mathbf{q},P,\nu) \in [\![s]\!]}$ $\mathcal{L}(\mathbf{q},P,\nu)$ ($\mathcal{A}^e \times \overline{B}$) where $[\![s]\!]$ = { $(\mathbf{q},P,\nu) \mid \nu \models \Phi$ }-i.e. the set of configurations of the product automaton $A^e \times \overline{B}$ represented by the given product state *s*.

Definition 3 Given a GRAN *A* and a GRA *B*, a partial order \subseteq is a *subsumption* provided that, for any two product states *s* and *t*, we have $s \subseteq t$ only if $\mathcal{L}_s(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B})$.

A procedure for checking the emptiness of $\mathcal{A}^e \times \overline{B}$ needs not continue the search from a product state *s* if it has already visited a product state *t* that subsumes *s*. The intuition is that any counterexample discovered from *s* can also be discovered from *t*. The trace inclusion semi-algorithm described below in Sect. [4.4](#page-16-0) works, in principle, with any given subsumption relation. In practice, our implementation uses the subsumption relation defined by the lemma below:

Lemma 4 *The relation defined such that* $(\mathbf{q}, P, \Phi) \sqsubseteq_{\text{imp}} (\mathbf{r}, S, \Psi) \iff \mathbf{q} = \mathbf{r}, P \supseteq P$ *S, and* $\Phi \rightarrow \Psi$ *is a subsumption.*

Proof For any valuation $\nu \in \mathcal{D}^{X,\mathcal{A}}$, we have $\mathcal{L}_{(\mathbf{q},P,\nu)}(\mathcal{A}^e \times \overline{B}) = \mathcal{L}_{(\mathbf{q},\nu)}(\mathcal{A}^e) \cap \mathcal{L}_{(P,\nu)}(\overline{B})$. Since $P \supseteq S$, we have $\mathcal{L}_{(P,\nu)}(B) \supseteq \mathcal{L}_{(S,\nu)}(B)$, thus $\mathcal{L}_{(P,\nu)}(\overline{B}) \subseteq \mathcal{L}_{(S,\nu)}(\overline{B})$. We obtain that $\mathcal{L}(\mathbf{q},P,\nu)(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}(\mathbf{r},\nu)(\mathcal{A}^e) \cap \mathcal{L}(\mathbf{S},\nu)(\overline{B}) = \mathcal{L}(\mathbf{r},\mathbf{S},\nu)(\mathcal{A}^e \times \overline{B})$. Since moreover $\Phi \to \Psi$, we have that $\mathcal{L}_{(\mathbf{q},P,\Phi)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_{(\mathbf{r},S,\Phi)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_{(\mathbf{r},S,\Psi)}(\mathcal{A}^e \times \overline{B}).$

Example 6 In the antichain from Fig. [2d](#page-4-0), $((q_1^1, q_1^2), \{p_1\}, v = 1) \sqsubseteq_{img} ((q_1^1, q_1^2), \{p_1\}, v =$ 1) because $\langle q_1^1, q_1^2 \rangle = \langle q_1^1, q_1^2 \rangle$, $\{p_1\} \supseteq \{p_1\}$, and $v = 1 \rightarrow v = 1$.

The language inclusion algorithm for non-deterministic automata on finite alphabets [\[1\]](#page-32-12) uses also a more sophisticated subsumption relation based on a pre-computed simulation [\[29\]](#page-32-14) between the states of the automata. We have defined a similar notion of simulation for generic register automata and an algorithm for computing such simulations. Details concerning data simulations and their integration within the framework of antichain-based abstraction refinement are described in Sect. [5.](#page-21-0)

4.4 The trace inclusion semi-algorithm

With the previous definitions, Algorithm [1](#page-17-0) describes the procedure for checking trace inclusion. It uses a classical worklist iteration loop (lines 2–30) that builds an antichain tree by simultaneously unfolding the expansion A^e of the network A and the complement \overline{B} of the the observer *B*, while searching for a counterexample trace $w \in \mathcal{L}(\mathcal{A}^e \times B)$. Both \mathcal{A}^e and *B* are built on-the-fly, during the abstract state space exploration.

Within Algorithm [1,](#page-17-0) the antichain is represented as a set of nodes. Each node is a tuple $\langle s, p \rangle$ where *s* is a product state and *p* is a position in the tree. The processed antichain nodes are kept in the set Visited, and their abstract successors, not yet processed, are kept in the set Next. Initially, Visited = Ø and Next = $\{(root_{A,B}, \epsilon)\}$. The algorithm uses a predicate map Π , which is initially empty (line 1).

We keep a set of subsumption edges Subsume ⊆ Visited × (Visited ∪ Next) with the following meaning: $(\langle s, p \rangle, \langle t, q \rangle) \in$ Subsume for two antichain nodes, where *s*, *t* are product states and $p, q \in \mathbb{N}^*$ are tree positions, if and only if there exists an abstract successor $s' \in Post_{\Pi}(s)$ such that $s' \sqsubseteq t$ (Definition [3\)](#page-16-1). Observe that we do not explicitly store a subsumed successor of a product state *s* from the antichain; instead, we add a subsumption

Algorithm 1 Trace Inclusion Semi-algorithm

input:

1. A GRAN $\mathcal{A} = \langle A_1, \ldots, A_N \rangle$ such that $A_i = \langle \mathcal{D}, \Sigma_i, \mathbf{x}_i, Q_i, \iota_i, F_i, \Delta_i \rangle$ for all $i \in [1, N]$. 2. A GRA $B = \langle \mathcal{D}, \Sigma, \mathbf{x}_B, Q_B, \iota_B, F_B, \Delta_B \rangle$ such that $\mathbf{x}_B \subseteq \bigcup_{i=1}^N \mathbf{x}_i$. **output**: True if $\mathcal{L}(\mathcal{A}) \downarrow_{\mathbf{X}(\mathcal{B})} \subseteq \mathcal{L}(B)$, otherwise a trace $\tau \in \mathcal{L}(\mathcal{A}) \downarrow_{\mathbf{X}(\mathcal{B})} \setminus \mathcal{L}(B)$. 1: $\Pi \leftarrow \emptyset$, Visited $\leftarrow \emptyset$, Next $\leftarrow \langle root_{(\mathcal{A}, B)}, \varepsilon \rangle$, Subsume $\leftarrow \emptyset$ 2: **while** Next $\neq \emptyset$ **do**
3: choose curr \in N 3: choose curr ∈ Next and move curr from Next to Visited 4. match curr with (s, n) 4: match curr with $\langle s, p \rangle$
5: **if** *s* is an accepting prod 5: **if** *s* is an accepting product state **then**
6: let ρ be the path from the root to cu 6: let ρ be the path from the root to curr and *k* be the pivot of ρ
7: **if** $k > 0$ then 7: **if** $k \ge 0$ **then**
8: $\Pi \leftarrow$ REEI 8: $\Pi \leftarrow \text{REFINEPREDICATEMAPBYINTERPOLATION}(\Pi, \rho, k)$
 $\rho \leftarrow \text{SIERE}(\rho_k)$ 9: $\text{rem} \leftarrow \text{SUBTree}(\rho_k)$
10: $\text{for } (n, m) \in \text{Subsum}$ 10: **for** $(n, m) \in$ Subsume such that $m \in$ rem **do** 11: move *n* from Visited to Next 11: move *n* from Visited to Next
12: remove rem from (Visited, Next 12: remove rem from (Visited, Next, Subsume)
13: $ad \omega$ to Next 13: add ρ_k to Next
14: **else** 14: **else** return EXTRACTCOUNTEREXAMPLE (ρ) 16: **else** 17: $i \leftarrow 0$
18: **for** $t \in$ 18: **for** $t \in Post_{\Pi}(s)$ **do**
19: **if** there exists *m* : 19: **if** there exists $m = \langle t', p' \rangle \in \text{Visual}$ such that $t \sqsubseteq t'$ then 20: add (curr, *m*) to Subsume 21: 21:
?? 22: $\qquad \qquad \text{rem} \leftarrow \{n \in \text{Next} \mid n = \langle t', p' \rangle \text{ and } t' \sqsubset t\}$ 23: $\sec \leftarrow \langle t, p.i \rangle$
24: $i \leftarrow i+1$ 24: $i \leftarrow i + 1$
25: **for** $n \in V$ 25: **for** $n \in V$ is ited such that *n* has a successor $m \in r$ em **do** 26: **add** (*n*, succ) to Subsume 26: add $(n, succ)$ to Subsume
27: **for** $(n, m) \in$ Subsume such th 27: **for** $(n, m) \in$ Subsume such that $m \in$ rem **do** 28: **add** (n, succ) to Subsume add $(n, succ)$ to Subsume 29: remove rem from (Visited, Next, Subsume)
30: add succ to Next add succ to Next 31: return *True*

edge between the node labeled with *s* and the node that subsumes that particular successor. The algorithm terminates when each abstract successor of a node from Next is subsumed by some node from Visited.

An iteration of Algorithm [1](#page-17-0) starts by choosing a current antichain node curr = $\langle s, p \rangle$ from Next and moving it to Visited (line 3). If the product state *s* is accepting (line 5), we check the counterexample path ρ , from the root of the antichain to curr, for spuriousness, by computing its pivot *k* (see Sect. [4.2\)](#page-14-1). If $k \ge 0$, then ρ is a spurious counterexample (line 7), and the path formula of the suffix of ρ , which starts with position k , is infeasible. In this case, we compute an interpolant for the suffix and refine the current predicate map Π by adding the predicates from the interpolant to the corresponding substates of the product states from the suffix (line 8).

The function REFINEPREDICATEMAPBYINTERPOLATION updates the predicate map using the principle described in Sect. [4.2.](#page-14-1) Subsequently, we remove (line 12) from the current antichain the subtree rooted at the pivot node ρ_k , i.e., the *k*th node on the path ρ (line 9), and add ρ_k to Next in order to trigger a recomputation of this subtree with the new predicate map. Moreover, all nodes with a successor previously subsumed by a node in the removed subtree are moved from Visited back to Next in order to reprocess them (line 11).

On the other hand, if the counterexample ρ is found to be real ($k = -1$), any valuation $v \in \bigcup_{i=0}^{|\rho|} \mathcal{D}^{\mathbf{x}^i}$ that satisfies the path formula $\Theta(\rho)$ yields a counterexample trace $w \in \mathcal{C}(\Lambda)$. $\mathcal{L}(\mathcal{A}) \downarrow_{\mathbf{x}_B} \setminus \mathcal{L}(B)$, obtained by ignoring all variables from $\mathbf{x}_A \setminus \mathbf{x}_B$ (line 15).

If the current node is not accepting, we generate its abstract successors (line 18). In order to keep in the antichain only nodes that are incomparable wrt the subsumption relation \subseteq , we add a successor *t* of *s* to Next (lines 23 and 30) only if it is not subsumed by another product state from a node $m \in V$ is ited. Otherwise, we add a subsumption edge (curr, *m*) to the set Subsume (line 20). Furthermore, if *t* is not subsumed by another state in Visited, we remove from Next all nodes $\langle t', p' \rangle$ such that *t* strictly subsumes *t'* (lines 22 and 29) and add subsumption edges to the node storing *t* from all nodes with a removed successor (line 26) or a removed subsumption edge (line 28).

The following theorem states the soundness of our trace inclusion semi-algorithm.

Theorem 1 Let $A = \langle A_1, \ldots, A_N \rangle$ be a GRAN such that $A_i = \langle D, \Sigma_i, \mathbf{x}_i, Q_i, \iota_i, F_i, \Delta_i \rangle$ for *all* $i \in [1, N]$, and let $B = \langle \mathcal{D}, \Sigma, \mathbf{x}_B, Q_B, \iota_B, F_B, \Delta_B \rangle$ be a GRA such that $\mathbf{x}_B \subseteq \bigcup_{i=1}^N \mathbf{x}_i$. *If Algorithm* [1](#page-17-0) *terminates and returns true on input A and B, then* $\mathcal{L}(\mathcal{A})\downarrow_{\mathbf{X}_R} \subseteq \mathcal{L}(B)$ *.*

The dual question "if there exists a counterexample trace $w \in \mathcal{L}(\mathcal{A}) \downarrow_{\mathbf{x}_R} \setminus \mathcal{L}(B)$, will Algorithm [1](#page-17-0) discover it?" can also be answered positively, using an implementation that enumerates the abstract paths in a systematic way, e.g., by using a breadth-first path exploration. This can be done using a queue to implement the Next set in Algorithm [1.](#page-17-0)

4.5 Proof of Theorem [1](#page-18-0)

Given a network $A = \langle A_1, \ldots, A_N \rangle$ where $A_i = \langle D, \Sigma_i, \mathbf{x}_i, Q_i, \iota_i, F_i, \Delta_i \rangle$ for all $i \in [1, N]$ and an observer $B = \langle D, \Sigma, x_B, Q_B, \iota_B, F_B, \Delta_B \rangle$, we recall that a configuration of the product automaton $\mathcal{A}^e \times \overline{B}$ is a tuple $((q_1, \ldots, q_N), P, \nu) \in Q_1 \times \cdots \times Q_N \times 2^{Q_B} \times \mathcal{D}^{\mathbf{x}}\mathcal{A},$ and a node of the antichain *T* is a pair $\langle s, p \rangle$ where *s* is a product state for *A* and *B* and $p \in \mathbb{N}^*$ is a tree position. Moreover, $root_{\langle A,B \rangle} = (\langle \iota_1, \ldots, \iota_N \rangle, \{ \iota_B \}, \top)$ is the product state that labels the root of *T*. In the following, let $\Gamma = (\Pi, \text{Visited}, \text{Next}, \text{Subsum})$ be an *antichain state* where Π is the predicate map, and Visited, Next, and Subsume are the sets of antichain nodes handled by Algorithm [1.](#page-17-0)

We say that Γ is a *closed antichain state* if and only if, for all nodes $\langle s, p \rangle \in \mathbb{V}$ is ited and every successor $(\mathbf{q}, P, v) \in succ_{A^e \times \overline{B}}([\![s]\!])$ of a configuration of the product automaton $A^e \times \overline{B}$ represented by the product state *s*, there exists a node $\langle t, r \rangle \in \forall$ is ited ∪ Next such that $\mathcal{L}_{(\mathbf{q},P,\nu)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B})$ and one of the following holds:

- $r = r = p \cdot i$ for some $i \in \mathbb{N}$, i.e., $\langle t, r \rangle$ is a child of $\langle s, p \rangle$ in the antichain $T = \text{Visited } \cup \text{V}$ Next, or
- $(\langle s, p \rangle, \langle t, r \rangle)$ ∈ Subsume.

In other words, the current antichain \mathcal{T} , defined as the union of the sets \forall is ited and Next, is in a closed state if the residual language of every successor of a configuration of the product automaton $A^e \times B$ that is covered by a visited product state must be included in the residual language of a product state stored in the antichain, either as a direct successor in the tree or via a subsumption edge.

For a product state *s*, we define $Dist(s) = \min \left\{ |w| | w \in \mathcal{L}_s(\mathcal{A}^e \times \overline{B}) \right\}$, and $Dist(s) = \infty$ if and only if $\mathcal{L}_s(\mathcal{A}^e \times \overline{B}) = \emptyset$. For a finite non-empty set of antichain nodes *S*, we define $Dist(S) = \min \{Dist(s) | \langle s, p \rangle \in S \}$ with $Dist(\emptyset) = \infty$.

We now prove several auxiliary lemmas.

Lemma 5 *Given a network A and an observer B, for any product state s of A and B, we* h *ave* succ_{*A*^{*e*} × \overline{B}}([[*s*]]) = $\bigcup_{t \in Post(s)}$ [[*t*]].

Proof Let $s = (\mathbf{q}, P, \Phi)$. " \subseteq " Let $(\mathbf{r}, S, \mu) \in succ_{A^e \times \overline{B}}([\![s]\!])$ be a configuration of $A^e \times \overline{B}$ for which there exists $(\mathbf{q}, P, \nu) \in [\![s]\!]$ such that $(\mathbf{q}, P, \nu) \xrightarrow{\sigma, \theta} (\mathbf{r}, S, \mu)$. Then there exists a unique rule $(\mathbf{q}, P) \xrightarrow{\sigma, \theta} (\mathbf{r}, S) \in \Delta^p$ such that $(\nu, \mu) \models \theta$. Moreover, if $(\mathbf{q}, P, \nu) \in [\![s]\!]$, we have $\nu \models \Phi$. Let $t = (\mathbf{r}, S, \Psi) \in Post(s)$ where $\Psi(\mathbf{x}_\mathcal{A}) \equiv \exists \mathbf{x}'_\mathcal{A} \cdot \Phi(\mathbf{x}'_\mathcal{A}) \wedge \theta(\mathbf{x}'_\mathcal{A}, \mathbf{x}_\mathcal{A})$. We have $\mu \models \Psi$, hence $(\mathbf{r}, S, \mu) \in [\![t]\!]$. " \supseteq " Let $(\mathbf{r}, S, \mu) \in [\![t]\!]$ for some $t \in Post(s)$. Then we have $t = (\mathbf{r}, S, \Psi)$ where $\Psi(\mathbf{x}_\mathcal{A}) \equiv \exists \mathbf{x}'_\mathcal{A} \cdot \Phi(\mathbf{x}'_\mathcal{A}) \wedge \theta(\mathbf{x}'_\mathcal{A}, \mathbf{x}_\mathcal{A})$. Since $\mu \models \Psi$, there exists $\nu \models \Phi$ such that $(\mathbf{q}, P, \nu) \stackrel{\sigma, \theta}{\rightarrow} (\mathbf{r}, S, \mu)$. Hence $(\mathbf{q}, P, \nu) \in [\![s]\!]$, thus $(\mathbf{r}, S, \mu) \in$ $succ_{A^e \times \overline{B}}([\llbracket s \rrbracket])$.

Lemma 6 *Given a network A, an observer B, and a predicate map* Π *, for any product state s* $of \mathcal{A}^e \times \overline{B}$ and any product state $t \in Post(s)$, there exists $t' \in Post_\Pi(s)$ such that $[\![t]\!] \subseteq [\![t']\!]$.

Proof Let $t = (\mathbf{r}, S, \Psi) \in Post(s)$. By the definition of $Post_{\Pi}$, we have $t' = (\mathbf{r}, S, \Psi^{\sharp}) \in$ *Post* $\Pi(s)$, where $\Psi \to \Psi^{\sharp}$, thus $[[t]] \subseteq [[t']$ \Box

Lemma 7 *Given a network A, an observer B, and a predicate map* Π *, for each product state s and each configuration* $(q, P, v) \in succ_{A^e \times \overline{B}}([s])$ *there exists a product state* $t \in Post_\Pi(s)$ *such that* $(\mathbf{q}, P, \nu) \in [[t]]$ *.*

Proof We use the fact that $succ_Ae\times\overline{B}(\llbracket s\rrbracket) = \bigcup_{t\in Post(s)} \llbracket t\rrbracket$ (Lemma [5\)](#page-18-1) and that for each \leq $t \in Post(s)$ there exists $t' \in Post_{\Pi}(s)$ such that $[[t]] \subseteq [[t']]$ (Lemma [6\)](#page-19-0).

The proof of soundness of Algorithm [1](#page-17-0) relies on the inductive invariants (Inv_1) and (Inv_2) from the following lemma.

Lemma 8 *The following invariants hold each time line* 2 *is reached in Algorithm* [1](#page-17-0)*:*

- $-I(nv_1)$ $\Gamma = (\Pi, \text{Visited}, \text{Next, Subsum})$ *is closed,*
- $-$ (Inv_2) $Dist(root_{(A,B)}) < \infty \rightarrow Dist(Visited) > Dist(Next)$.

Proof Initially, when coming to line 2 for the first time, we have Visited $= \emptyset$, thus $Dist(\forall \text{isited}) = \infty$, and both invariants hold trivially. For the case when coming to line 2 after executing the loop body, we denote by:

> $\Gamma_{old} = (\Pi_{old}, \text{Visited}_{old}, \text{Next}_{old}, \text{Subsum}_{old})$ and $\Gamma_{new} = (\Pi_{new}, \text{Visited}_{new}, \text{Next}_{new}, \text{Subsum}_{new})$

the antichain states before and after the execution of the main loop. We assume that both invariants hold for Γ_{old} .

 (Im_1) Let $\langle s, p \rangle \in \text{Visited}_{new}$ and $(q, P, v) \in succ_{\mathcal{A}^e \times \overline{B}}([\![s]\!])$. We distinguish two cases according to the control path taken inside the main loop: (1) If the test on line 5 is positive, the predicate map is augmented, i.e., $\Pi_{new} \supseteq \Pi_{old}$ (line 8). Let $\Gamma' =$ $(\Pi_{new}, \forall \text{isited}_{old}, \text{Next}_{old}, \text{Subsum}_{old})$ be the next antichain state. Clearly Γ' is closed provided that Γ_{old} is. Next, let $n_{pivot} \in Visted_{old}$ be the pivot of the path to the current node (line 6) and define the following sets of nodes:

$$
T = \text{SUBTree} n_{pivot},
$$

\n
$$
S = \{ n \in \text{Visited}_{old} \mid \exists m \in T. (n, m) \in \text{Subsumel} \}.
$$

Then we obtain (lines 10–13):

Visited_{new} = Visited_{old} \ $(S \cup T)$, $\text{Next}_{new} = ((\text{Next}_{old} \cup S) \setminus T) \cup \{n_{pivot}\},$ $\text{Visited}_{new} \cup \text{Next}_{new} = ((\text{Visited}_{old} \cup \text{Next}_{old}) \setminus T) \cup \{n_{pivot}\}.$

Since Γ' is closed, there exists a node $\langle t, r \rangle \in \text{Visited}_{old} \cup \text{Next}_{old}$ such that $\mathcal{L}_{(\mathbf{q}, P, \nu)}(\mathcal{A}^e \times \mathcal{A}^e)$ \overline{B}) \subset $\mathcal{L}_t(\mathcal{A}^e \times \overline{B})$ and either $r = p \cdot i$ for some $i \in \mathbb{N}$ or $(\langle s, p \rangle, \langle t, r \rangle) \in$ Subsume_{old}. We distinguish two cases:

- (a) $\langle t, r \rangle \notin T$. Then $\langle t, r \rangle \in \text{Visited}_{new} \cup \text{Next}_{new}$ and, because Subsume_{new} = Subsume_{old} ∩ (Visited_{new} × (Visited_{new} ∪ Next_{new})), we obtain that Γ_{new} is closed as well.
- (b) $\langle t, r \rangle \in T$. Then we distinguish two further cases:
	- (i) If $r = p \cdot i$ for some $i \in \mathbb{N}$, since we have assumed that $\langle s, p \rangle \in \mathbb{V}$ is ited_{new}, we have $\langle s, p \rangle \notin T$. The only possibility is then $\langle t, r \rangle = n_{pivot}$ and $\langle s, p \rangle$ is the parent of n_{pivot} . In this case, we have $\langle t, r \rangle \in \text{Next}_{new}$.
	- (ii) If $(\langle s, p \rangle, \langle t, r \rangle) \in$ Subsume_{old}, then $\langle s, p \rangle \in S$, which contradicts the assumption $\langle s, p \rangle \in \forall$ isited_{new}.
- (2) Otherwise, the test on line 5 is negative, in which case we have $\Pi_{new} = \Pi_{old}$ and Visited_{new} = Visited_{old} ∪ {curr}. For each $(\mathbf{q}, P, \nu) \in succ_{\mathbf{A}e \times \mathbf{B}}([\![s]\!])$ there exists $t \in Post_{\Pi}(s)$ such that $\mathcal{L}_{(\mathbf{q},P,\nu)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B})$ (by Lemma [7\)](#page-19-1). We distinguish two cases:
	- (a) $\langle s, p \rangle$ = curr. In this case, either (i) there is $\langle t', p' \rangle \in \text{Visted}_{old}$ such that *t* \subseteq *t'*, and then we also have $\mathcal{L}_{(\mathbf{q},P,\nu)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_{t'}(\mathcal{A}^e \times \overline{B})$ (Definition [3\)](#page-16-1) and $((s, p), (t', p')) \in$ Subsume_{new} (added on line 20), or (ii) $(t, p.i) \in$ Next_{new} for some $i \in \mathbb{N}$ (added on lines 23 and 30).
	- (b) Otherwise $\langle s, p \rangle$ ∈ Visited_{old}. As Γ' is closed, there is $\langle u, r \rangle$ ∈ Visited_{old} ∪ Next_{old} such that $\mathcal{L}_{(\mathbf{q},P,\nu)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_{\mathcal{U}}(\mathcal{A}^e \times \overline{B})$ and either $r = p.i$ for some $i \in \mathbb{N}$ or $(\langle s, p \rangle, \langle u, r \rangle) \in$ Subsume_{old}. We distinguish two sub-cases:
		- (i) $\langle u, r \rangle \in \text{rem (line 22). Then } \mathcal{L}_u(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B}) \text{ (Definition 3)},$ $\langle u, r \rangle \in \text{rem (line 22). Then } \mathcal{L}_u(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B}) \text{ (Definition 3)},$ $\langle u, r \rangle \in \text{rem (line 22). Then } \mathcal{L}_u(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B}) \text{ (Definition 3)},$ hence $\mathcal{L}_{(\mathbf{q},P,\nu)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B})$. If $r = p.i$, then $(\langle s, p \rangle, \langle t, r' \rangle) \in$ Subsume_{new} for some $r' \in \mathbb{N}^*$ (added on line 26). Else, if $(\langle s, p \rangle, \langle u, r \rangle) \in$ Subsume_{old}, we have $(\langle s, p \rangle, \langle t, r' \rangle) \in$ Subsume_{new} for some $r' \in \mathbb{N}^*$ (added on line 28). In both cases, we obtain that Γ_{new} is closed.
		- (ii) $\langle u, r \rangle \notin$ rem. Then $\langle u, r \rangle \in \text{Visited}_{new} \cup \text{Next}_{new}$. Since Subsume_{new} = Subsume*old* ∩ (Visited*new* × (Visited*new* ∪ Next*new*)), we obtain that Γ_{new} is closed.

(*Inv*2) We distinguish two cases:

- 1. If $Dist(\text{Visited}_{new}) = \infty$, it is sufficient to show that $Dist(\text{Next}_{new}) < \infty$. Suppose, by contradiction, that $Dist(Next_{new}) = \infty$, hence $Dist(\text{Visited}_{new} \cup Next_{new}) =$ ∞ , and since $root_{\langle A,B \rangle} \in \text{Visited}_{new} \cup \text{Next}_{new}$, we obtain $Dist(root_{\langle A,B \rangle}) = \infty$, contradiction.
- 2. Otherwise, $Dist(\text{Visited}_{new}) < \infty$ and there exists a node $\langle s, p \rangle \in \text{Visited}_{new}$ such that $Dist(\text{Visited}_{new}) = Dist(s) < \infty$. Let $w = (v_0, \sigma_0), (v_1, \sigma_1), \ldots, (v_n, \diamond) \in$ $\mathcal{L}_{s}(\mathcal{A}^e \times B)$ be a trace such that *Dist*(Visited_{new}) = *n*. Then there exists a run $(\mathbf{q}_0, P_0, v_0) \stackrel{\sigma_0}{\rightarrow} (\mathbf{q}_1, P_1, v_1) \stackrel{\sigma_1}{\rightarrow} \cdots \stackrel{\sigma_{n-1}}{\rightarrow} (\mathbf{q}_n, P_n, v_n)$ of $\mathcal{A}^e \times \overline{B}$ over w such that $(\mathbf{q}_0, P_0, v_0) \in [\![s]\!]$ and (\mathbf{q}_n, P_n) a final state of $\mathcal{A}^e \times \overline{B}$. Since Γ_{new} is closed

due to (Inv_1) and $(\mathbf{q}_1, P_1, \nu_1) \in succ_{\mathcal{A}^e \times \overline{B}}([\![s]\!])$, there exists a node $\langle s_1, p_1 \rangle \in \mathbb{V}$ isited_{new} \cup Next_{new} such that $\mathcal{L}_{(\mathbf{q}_1, P_1, \nu_1)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_{s_1}(\mathcal{A}^e \times \overline{B})$. If that $\mathcal{L}_{(\mathbf{q}_1, P_1, v_1)}(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_{s_1}(\mathcal{A}^e \times \overline{B})$. If $\langle s_1, p_1 \rangle \in \text{Next}_{new}$, we obtain that $Dist(\text{Next}_{new}) \leq n - 1$, and we are done. Otherwise, $\langle s_1, p_1 \rangle \in \text{Visited}_{new}$, and we can repeat the same argument inductively, to discover a sequence of nodes $\langle s_1, p_1 \rangle, \ldots, \langle s_n, p_n \rangle \in \forall$ is ited_{new} such that $\mathcal{L}(\mathbf{q}_i, P_i, v_i)$ ($\mathcal{A}^e \times \overline{B}$) $\subseteq \mathcal{L}_{s_n}(\mathcal{A}^e \times \overline{B})$ for all $i \in [1, n]$. Since (\mathbf{q}_n, P_n) is a final state of $A^e \times \overline{B}$, we have $(\nu_n, \diamond) \in \mathcal{L}_{(\mathbf{q}_i, P_i, \nu_i)}(\mathcal{A}^e \times \overline{B})$, thus $(\nu_n, \diamond) \in \mathcal{L}_{s_n}(\mathcal{A}^e \times \overline{B})$, and s_n is an accepting product state. But this contradicts with the fact that accepting product states are never stored in the antichain.

With the above lemmas at hand, we can finally prove Theorem [1:](#page-18-0)

Proof If Algorithm [1](#page-17-0) terminates and reports true, this is because Next = \varnothing , hence *Dist*(Next) = ∞. By Lemma [8](#page-19-2) (*Inv*₂), we obtain that *Dist(root*_{(A, B)}) = ∞. Suppose, by contradiction, that $\mathcal{L}(\mathcal{A})\downarrow_{\mathbf{x}_B} \nsubseteq \mathcal{L}(B)$. By Lemma [2,](#page-10-1) there exists a trace

 $w = (\nu_0, \sigma_0)(\nu_1, \sigma_1) \dots (\nu_n, \diamond) \in \mathcal{L}(\mathcal{A}^e \times \overline{B}).$

Thus we have a run of $A^e \times \overline{B}$ over w:

$$
(\mathbf{q}_0, P_0, \nu_0) \xrightarrow{\sigma_0} (\mathbf{q}_1, P_1, \nu_1) \xrightarrow{\sigma_1} \cdots \xrightarrow{\sigma_{n-1}} (\mathbf{q}_n, P_n, \nu_n)
$$

where $\mathbf{q}_0 = \langle \iota_1, \ldots, \iota_N \rangle$, $P_0 = \{ \iota_B \}$, \mathbf{q}_n is final in \mathcal{A}^e , $P_n \cap F_B = \emptyset$. But, since $(\mathbf{q}_0, P_0, \nu_0) \in$ $[root_{\langle A,B\rangle}]\]$, we have $w \in \mathcal{L}_{root_{\langle A,B\rangle}}(\mathcal{A}^e \times \overline{B})$. Hence, $Dist(root_{\langle A,B\rangle}) \leq n$, which is in contradiction with the fact that $Dist(root_{\langle A,B \rangle}) = \infty$. Consequently, it must be the case that $C(A)|_{\infty} \subseteq C(B)$. $\mathcal{L}(\mathcal{A})\downarrow_{\mathbf{X}R}\subseteq\mathcal{L}(B).$

5 Simulations on generic register automata

In the classical setting of finite state automata over finite alphabets, a *simulation* [\[29](#page-32-14)] is a relation on the states of an automaton which is invariant with respect to its transition relation. The simulation-based approach to checking language inclusion between two automata *A* and *B* first computes a simulation relation on the union of the states of *A* and *B*, and then checks whether the pair of initial states is a member of the simulation relation. Note that this is not a complete decision procedure for language inclusion, because there exist automata such that $\mathcal{L}(A) \subseteq \mathcal{L}(B)$, but the initial state of *A* is not simulated by the initial state of *B*. However, a pre-computed simulation relation can be used to speed up the convergence of the antichain-based method, by weakening (i.e. generalizing) the subsumption relation used by the antichain construction algorithm [\[1\]](#page-32-12). In practice, the experimental evaluation in [\[1\]](#page-32-12) shows a significant improvement of running times, when simulations are used.

In the below subsection, we first introduce a concept of *data simulations* suitable for GRAs, together with an algorithm that computes useful under-approximations of the largest data simulation on a given GRA. In the next subsection, we then propose a way of using data simulations to enhance the convergence of Algorithm [1](#page-17-0) between a GRAN and a GRA in a similar way as classical simulations are integrated with the antichain-based language inclusion algorithm for automata over finite alphabets [\[1\]](#page-32-12).

We note that, in the context of classical automata, an approach going beyond the combination of antichains and simulation relations has been proposed [\[7\]](#page-32-26). It is based on using *congruence relations* instead of antichains. However, their usage in the context of generic register automata is so far unclear, and we leave it as an interesting subject for future work.

5.1 Data simulations and their computation

Our notion of data simulations is defined as follows.

Definition 4 A relation $R \subseteq O \times D^x \times O$ is a *data simulation* for a GRA $A =$ $\langle \Sigma, \mathcal{D}, \mathbf{x}, \mathcal{Q}, \iota, F, \Delta \rangle$ if and only if the following holds for all $(q, \nu, q') \in R$:

- 1. $q \in F \Longrightarrow q' \in F$, and
- 2. for all $\sigma \in \Sigma$ and all $(r, v') \in Q \times \mathcal{D}^{\mathbf{x}}$ such that $(q, v) \stackrel{\sigma}{\to} {}_{A}(r, v')$ there exists $r' \in Q$ such that $(q', v) \xrightarrow{\sigma} {}_A (r', v')$ and $(r, v', r') \in R$.

Observe that, while a classical simulation is a binary relation on states, a data simulation is a ternary relation that involves also a valuation of the variables. The following lemma shows that a data simulation preserves the residual languages of GRAs:

Lemma 9 *Given a GRA A* = $\langle \Sigma, \mathcal{D}, \mathbf{x}, \mathcal{Q}, \iota, F, \Delta \rangle$ and a data simulation R $\subseteq Q \times \mathcal{D}^{\mathbf{x}} \times Q$ *for A, we have* $\mathcal{L}_{(q,\nu)}(A) \subseteq \mathcal{L}_{(q',\nu)}(A)$ *for any tuple* $(q,\nu,q') \in R$.

Proof Let $(v_0, \sigma_0), \ldots, (v_n, \diamond) \in \mathcal{L}_{(q,v)}(A)$ be a trace and $(q, v) = (q_0, v_0) \stackrel{\sigma_0}{\rightarrow} \cdots \stackrel{\sigma_{n-1}}{\rightarrow}$ (q_n, v_n) be a run of *A*. By induction on $n \ge 0$, it is easy to find a run $(q', v) = (q'_0, v_0) \stackrel{\sigma_0}{\rightarrow}$ \cdots $\xrightarrow{\sigma_{n-1}} (q'_n, v_n)$ of *A* such that, for all $i \in [0, n]$, $(q_i, v_i, q'_i) \in R$ and moreover, $q_i \in R$ $F \Longrightarrow q'_i \in F$. Thus, $(v_0, \sigma_0), \ldots, (v_n, \diamond) \in \mathcal{L}_{(q',v)}(A)$.

Let $A = \langle \Sigma, \mathcal{D}, \mathbf{x}, \mathcal{Q}, \iota, F, \Delta \rangle$, where $\mathcal{Q} = \{q_1, \dots, q_k\}$ for some $k > 0$, be a GRA for the rest of this section. The data simulation algorithm (Algorithm [2\)](#page-23-0) given in this section manipulates sets of valuations from $\mathcal{D}^{\mathbf{x}}$ that are definable by first-order formulae in Form(\mathcal{D}). A relation $R \subseteq Q \times D^x \times Q$ is said to be *definable* if and only if there exists a matrix $\Phi = [\phi_{ij}]_{i,j=1}^k$ of formulae $\phi_{ij}(\mathbf{x}) \in \text{Form}(\mathcal{D})$ such that $(q_i, v, q_j) \in R \iff v \models \phi_{ij}$. For $\ell \in [1, k]$, we denote by Φ_{ℓ} the ℓ th row of the matrix Φ .

Algorithm [2](#page-23-0) is a refinement algorithm which handles two matrices of formulae that define the relations *Sim, PrevSim* $\subseteq Q \times D^{\mathbf{x}} \times Q$. Below, we shall use the same names to denote the relations and their matrix representations. Intuitively, *PrevSim* is the previous candidate for simulation, whereas *Sim* is an entry-wise stronger relation that refines *PrevSim*. The refinement step is performed backwards wrt each transition rule $q_i \stackrel{\sigma,\phi}{\longrightarrow} q_\ell$ of the automaton as follows. The tuple (q_i, v, q_j) is added to the newly created relation *Sim* if $(q_i, v, q_j) \in$ *PrevSim* and there exist a valuation v' , a state $q_m \in Q$, and a formula ψ such that $(v, v') \models \phi$, $q_j \stackrel{\sigma,\psi}{\longrightarrow} q_m$, $(q_\ell, v', q_m) \in \text{PrevSim}$, and $(v, v') \models \psi$. This update guarantees that, for every transition $(q_i, v) \stackrel{\sigma}{\rightarrow} A (q_\ell, v')$ where $(q_i, v, q_j) \in Sim$, there exists a state q_m such that $(q_j, v) \stackrel{\sigma}{\rightarrow} A(q_m, v')$ and $(q_\ell, v', q_m) \in \text{PrevSim.}$ The algorithm stops when *Sim* and *PrevSim* define the same relation. Moreover, this relation is guaranteed to be a data simulation.

To define the update, we use the following function, where $\sigma \in \Sigma$ is an input event, *i*, *j*, $\ell \in [1, k]$ are state indices such that $q_i \stackrel{\sigma, \phi}{\longrightarrow} q_j \in \Delta$ is a transition rule and *R* is $k \times k$ matrix of formulae:

$$
Presim_{\sigma}(i, j, \ell, R) \equiv \forall \mathbf{x}', \phi(\mathbf{x}, \mathbf{x}') \rightarrow \bigvee_{q_j \xrightarrow{\sigma, \psi} q_m} \psi(\mathbf{x}, \mathbf{x}') \wedge R_{\ell m}(\mathbf{x}') .
$$

We also define the sets $post_{\sigma}(q) = \{q' \mid q \stackrel{\sigma,\phi}{\longrightarrow} q' \in \Delta\}$ and $pre_{\sigma}(q) = \{q' \mid q' \stackrel{\sigma,\phi}{\longrightarrow} q \in \Delta\}.$ With this notation, Algorithm [2](#page-23-0) describes the procedure that computes a data simulation for a given data automaton.

Algorithm 2 Data Simulation Algorithm

input: A generic register automaton $A = \langle \Sigma, \mathcal{D}, \mathbf{x}, \mathcal{Q}, \iota, F, \Delta \rangle$, where $\mathcal{Q} = \{q_1, \dots, q_k\}$, and a constant $K > 0$. **output**: A data simulation $R \subseteq Q \times \mathcal{D}^X \times Q$ for *A*. **global vars** $[\textit{Sim}_{ij}]_{i,j=1}^k$, $[\textit{PrevSim}_{ij}]_{i,j=1}^k$, $[\textit{Cont}_{ij}]_{i,j=1}^k$ 1: **for** $i = 1, ..., k$ **do**
2: **for** $i = 1, ..., k$ 2: **for** $j = 1, ..., k$ **do**
3: **PrevSim_{ij}** $\leftarrow \top$ 3: $\text{PrevSim}_{ij} \leftarrow \top$
4: $\text{Cnt}_{ij} \leftarrow K$ $\textit{Cnt}_{ij} \leftarrow K$ 5: **for** $j = 1, ..., k$ **do**
6: **if** $a_i \in F$ and $a_i \notin F$ 6: **if** $q_i \in F$ and $q_j \notin F$ **then**
7: $\lim_{i \to \infty} \left(\frac{1}{f} \right)$ 7: $\sin_{ij} \leftarrow \perp$
8: **else** 8: **else** 9: $\sin_{ij} \leftarrow \bigwedge_{\sigma \in \Sigma} \bigwedge_{q_\ell \in post_\sigma(q_i)} \text{Presim}_\sigma(i, j, \ell, \text{Previous})$ 10: **while** $\exists \ell \in [1, k]$ such that $Sim_{\ell} \neq \text{PrevSim}_{\ell}$ **do** 11: *TempSim* ← *Sim* 11: $TempSim \leftarrow Sim$
12: $pick \ell \in [1, k]$ su 12: pick $\ell \in [1, k]$ such that $Sim_{\ell} \neq \text{PrevSim}_{\ell}$
13: **for** $\sigma \in \Sigma$ **do** 13: **for** $\sigma \in \Sigma$ **do** 14: **for** $q_i \in pre_{\sigma}(q_\ell)$ **do**
15: **for** $i = 1$ **k d** 15: **for** $j = 1, ..., k$ **do**
16: **Simi** \leftarrow **Simi** \wedge $Sim_{ij} \leftarrow Sim_{ij} \wedge PreSim_{\sigma}(i, j, \ell, TempSim)$ 17: **for** all $j = 1, ..., k$ such that $\text{Sim}_{\ell} \neq \text{PrevSim}_{\ell} \neq 18$: **if** $\text{Cont}_{\ell} = 0$ **then** 18: **if** $\text{Cnt}_{\ell j} = 0$ **then**
19: $\text{Sim}_{\ell i} \leftarrow \perp$ 19: $\lim_{\ell j} \leftarrow \perp$
20: **else** 20: **else** $\text{Cnt}_{\ell i} \leftarrow \text{Cnt}_{\ell i} - 1$ 22: $PrevSim_{\ell} \leftarrow TempSim_{\ell}$ 23: **return** *Sim*

Initially, the matrix *PrevSim* is true everywhere (line 3). The current simulation candidate *Sim* is initialized to false for all *i*, $j \in [1, k]$ such that $q_i \in F$ and $q_j \notin F$ (line 7). Observe that, in this case, q_i cannot simulate q_i , by Definition [4](#page-22-0) (1). Otherwise, we initialize Sim_{ij} to the strongest pre-simulation with respect to *PrevSim* (line 9). In the iterative loop (lines 10– 22), the algorithm chooses a state q_ℓ for which the current simulation candidate Sim_ℓ is not equivalent to the previous one $PrevSim_\ell$ (line 13) and sharpens the set Sim_{ij} with respect to the transition rule $q_i \stackrel{\sigma, \phi}{\longrightarrow} q_\ell$ for all input symbols $\sigma \in \Sigma$ and all peer states q_j , $j \in [1, k]$ (line 16). The following invariants are key to proving the correctness of Algorithm [2.](#page-23-0)

Lemma 10 *The following invariants hold each time Algorithm* [2](#page-23-0) *reaches line 10:*

- *–* (SimInv₁) for all *i*, *j* ∈ [1, *k*]*, the entailment Sim_i i → PrevSim_i is valid.*
- $(SimInv_2)$ *for all* $\sigma \in \Sigma$, *all* $i, j, \ell \in [1, k]$ *and all* $\nu, \nu' \in \mathcal{D}^{\mathbf{x}}$, *if* $\nu \models Sim_{ij}$ *and* $(q_i, \nu) \stackrel{\sigma}{\rightarrow}$ (q_{ℓ}, v') then there exists $m \in [1, k]$ such that $(q_j, v) \stackrel{\sigma}{\rightarrow} (q_m, v')$ and $v' \models \text{PrevSim}_{\ell m}$.

Proof Let Sim' and PrevSim' denote the global matrices after one iteration of the loop on lines 10–22.

 $(SimInv_1)$ When line 10 is reached for the first time, $PrevSim_{ii} = \top$ for all *i*, $j \in [1, k]$, thus *SimInv*₁ holds initially. Since *Sim* is modified on lines 16 or 19 only, we have $Sim'_{ij} \rightarrow Sim_{ij}$ for all $i, j \in [1, k]$. Moreover, for each $i, j \in [1, k]$ either (i) $\text{PrevSim}'_{ij} = \text{TempSim}_{ij} =$ Sim_{ij} (line 22) and $Sim'_{ij} \rightarrow Sim_{ij} \rightarrow PrevSim'_{ij}$ holds, or (ii) $PrevSim'_{ij} = PrevSim_{ij}$ (no update) and $Sim'_{ij} \rightarrow Sim_{ij} \rightarrow PrevSim_{ij} \rightarrow PrevSim'_{ij}$ holds, by the inductive hypothesis.

(*SimInv*₂) We show that this invariant holds the first time the control reaches line 10. Let $\sigma \in \Sigma$, *i*, *j*, $\ell \in [1, k]$ and $\nu, \nu' \in \mathcal{D}^{\mathbf{x}}$ such that $\nu \models \text{Sim}_{ij}$ and $(q_i, \nu) \stackrel{\sigma}{\rightarrow} (q_\ell, \nu')$. Since $\nu \models \text{Sim}_{ij}$ (thus $\text{Sim}_{ij} \neq \bot$) and $q_\ell \in \text{post}_{\sigma}(q_i)$, we have that $\nu \models \text{PreSim}_{\sigma}(i, j, \ell, \text{PrevSim})$ where $q_i \stackrel{\sigma, \phi}{\longrightarrow} q_\ell \in \Delta$. Since $(q_i, v) \stackrel{\sigma}{\rightarrow} (q_\ell, v')$, we obtain that $(v, v') \models \phi(\mathbf{x}, \mathbf{x}')$, and, consequently, $(v, v') \models \psi(\mathbf{x}, \mathbf{x}') \land \text{PrevSim}_{\ell m}(\mathbf{x}') \text{ for some } m \in [1, k] \text{ such that } q_j \stackrel{\sigma, \psi}{\longrightarrow}$ $q_m \in \Delta$. Hence, *SimInv*₂ holds when the control first reaches line 10.

For the induction step, let us assume that $\frac{SimInv_2}{N}$ holds on line 10, and we prove that it also holds after executing line 22. Let $\sigma \in \Sigma$, *i*, *j*, $\ell \in [1, k]$ and $\nu, \nu' \in \mathcal{D}^{\mathbf{x}}$ such that $\nu \models \textit{Sim}'_{ij}$ and $(q_i, \nu) \stackrel{\sigma}{\rightarrow} (q_\ell, \nu')$. We distinguish two cases:

- 1. If Sim_{ℓ} ≠ $Previous_{\ell}$ on line 10 since $q_i \in pre_{\sigma}(q_{\ell})$, then Sim'_{ij} was updated on line 16. Since $v \models Sim'_{ij}$, we obtain $v \models Presim_{\sigma}(q_i, q_j, q_\ell, Sim)$. Moreover, *PrevSim'_l* is updated to $TempSim_{\ell} \equiv Sim_{\ell}$ on line 22, hence $\nu \models Presim_{\sigma}(q_i, q_j, q_{\ell}, PrevSim')$ as well. Since $(q_i, v) \stackrel{\sigma}{\rightarrow} (q_\ell, v')$, we obtain that $(v, v') \models \psi(\mathbf{x}, \mathbf{x}') \land \text{PrevSim}_{\ell m}'(\mathbf{x}')$ for some $m \in [1, k]$ such that $q_j \stackrel{\sigma, \psi}{\longrightarrow} q_m \in \Delta$, thus $(\nu, \nu') \models \psi(\mathbf{x}, \mathbf{x}')$ and $\nu' \models \text{PrevSim}_{\ell m}'$. Thus $SimInv_2$ holds for Sim' and $PrevSim'$.
- 2. Otherwise $Sim_{\ell} \equiv \text{PrevSim}_{\ell}$ on line 10. Moreover, $\text{PrevSim}_{\ell} \equiv \text{PrevSim}_{\ell}$ because the update on line 22 is skipped, and, for all $q_i \in pre_\sigma(q_\ell)$ and all $j \in [1, k]$, we have $Sim'_{ij} \equiv Sim_{ij}$. Then, by the induction hypothesis, $SimInv_2$ holds for Sim' and $PrevSim'$ because it holds for *Sim* and *PrevSim*.

The algorithm iterates the loop on lines 10–22) until *Sim* and *PrevSim* define the same relation. Since, in general, the data constraints *Sim_{ij}* obtained from different iteration steps might form an infinitely decreasing sequence, we use the matrix *Cnt* of integer counters, initially set to some input value $K > 0$ (line 4).^{[15](#page-24-0)} Observe that each entry Cnt_{ij} decreases every time $Sim_{ij} \neq \text{PrevSim}_{ij}$ (line 21). When the counter Crit_{ij} reaches zero, we set Sim_{ij} to false (line 19), which guarantees that $Sim_{ij} \equiv \text{PrevSim}_{ij}$ always in the future. Since the number of entries in the counter matrix is finite, the algorithm is guaranteed to terminate. The following theorem summarizes the main result of this section.

Theorem [2](#page-23-0) *Algorithm* 2 *terminates on any GRA* $A = \langle \Sigma, \mathcal{D}, \mathbf{x}, Q, \iota, F, \Delta \rangle$ *, and its output is a data simulation* $R \subseteq Q \times D^X \times Q$ for A.

Proof Let *Simⁿ* and *PrevSimⁿ* denote the matrices *Sim* and *PrevSim* at the *n*th iteration of the loop on lines 10–[2](#page-23-0)2, for $n \ge 0$. Algorithm 2 terminates whenever $Sim_{ij}^n \equiv \text{PrevSim}_{ij}^n$ for all $i, j \in [1, k]$ (line 10). Suppose, by contradiction, that this never happens, thus there exist *i*, $j \in [1, k]$ such that $Sim_{ij}^n \neq \text{PrevSim}_{ij}^n$ for all $n \geq 0$. Then $\text{Cont}_{ij}^K = 0$ (line 21) and $Sim_{ij}^{K+1} = \text{PrevSim}_{ij}^{K+2} = \bot$ (lines 19 and 22). Since $Sim_{ij}^n \rightarrow \text{PrevSim}_{ij}^n$, by Lemma [10](#page-23-1) $(SimInv_1)$, we obtain that $Sim_{ij}^{K+2} = PrevSim_{ij}^{K+2}$, a contradiction.

To prove that the output of Algorithm [2](#page-23-0) is a data simulation for *A*, we use Lemma [10](#page-23-1) $(SimInv_2)$ and the fact that, upon termination, we have $Sim_{ij} \equiv \text{PrevSim}_{ij}$, for all *i*, $j \in [1, k]$. \Box

5.2 Simulation and subsumption

Finally, we explain how a data simulation relation computed by Algorithm [2](#page-23-0) can be used to optimize the trace inclusion semi-algorithm. Let $A = \langle A_1, \ldots, A_N \rangle$ be a GRAN where

¹⁵ Taking a bigger *K* leads to a more precise Sim_{ij} , but, on the other hand, it can significantly increase the computation time.

 $A_i = \langle \mathcal{D}, \Sigma_i, \mathbf{x}_i, Q_i, \iota_i, F_i, \Delta_i \rangle$ for all $i \in [1, N]$, and let $B = \langle \mathcal{D}, \Sigma, \mathbf{x}_B, Q_B, \iota_B, F_B, \Delta_B \rangle$ be an observer GRA such that $\mathbf{x}_B \subseteq \bigcup_{i=1}^N \mathbf{x}_i$.

The main problem for using data simulations to enhance the convergence of our trace inclusion semi-algorithm is related to the fact that simulation relations are, in general, not compositional wrt the interleaving semantics of the network. In other words, if we have *N* data simulations R_i ⊆ Q_i × \mathcal{D}^{X_i} × Q_i for $i \in [1, N]$, then their cross-product R ⊆ $Q_A \times D^{X_A} \times Q_A$ defined as:

$$
\forall q_1, r_1 \in Q_1 \dots \forall q_N, r_N \in Q_N \forall v
$$

\n
$$
\in \mathcal{D}^{X_{\mathcal{A}}} : (\langle q_1, \dots, q_N \rangle, v, \langle r_1, \dots, r_N \rangle) \in R \iff (q_i, v \downarrow_{X_i}, r_i) \in R_i
$$

is not necessarily a simulation on the network expansion A^e . The reason for this can be seen for $N = 2$. Let $\sigma_1, \sigma_2 \in \Sigma_A$ such that $\sigma_1 \notin \Sigma_2$ and $\sigma_2 \notin \Sigma_1$. The execution of \mathcal{A}^e on the sequence of input symbols $\sigma_1 \sigma_2$ is $(\langle q_1, q_2 \rangle, v) \stackrel{\sigma_1}{\rightarrow} (\langle q'_1, q_2 \rangle, v') \stackrel{\sigma_2}{\rightarrow} (\langle q'_1, q'_2 \rangle, v'')$. Suppose that $(q_i, v \downarrow_{\mathbf{x}_i}, r_i) \in R_i$, for all $i = 1, 2$. Then there exists $r'_1 \in Q_1$ such that $(\langle r_1, r_2 \rangle, v) \stackrel{\sigma_1}{\rightarrow} (\langle r'_1, r_2 \rangle, v')$ and $(q'_1, v' \downarrow_{\mathbf{x}_1}, r'_1) \in R_1$. In order to use the simulation and build the continuation $(\langle r'_1, r_2 \rangle, v') \stackrel{\sigma_2}{\rightarrow} (\langle r'_1, r'_2 \rangle, v'')$, we would need that $(q_2, v' \downarrow_{\mathbf{x}_2}, r_2) \in R_2$, which is not necessarily ensured by the hypothesis $(q_2, v \downarrow_{\mathbf{x}_2}, r_2) \in R_2$.

We propose a partial solution to this problem, based on a restriction concerning the distribution of the network variables $\mathbf{x}_A = \bigcup_{i=1}^N \mathbf{x}_i$ over the components A_1, \ldots, A_N : for each $i \in [1, N]$, we have $\mathbf{x}_i = \mathbf{x}^g \cup \mathbf{x}_i^{\ell}$ where \mathbf{x}^g is a set of *global* variables and \mathbf{x}_i^{ℓ} are the *local* variables of A_i . In other words, the only variables shared between more than one component are the global variables \mathbf{x}^g , which, moreover, are visible to all components.¹⁶ Then the problem can be bypassed if none of the simulation relations $R_i \subseteq Q_i \times D^{x_i} \times Q_i$ may constrain the global variables:

Assumption 3 *For each i* \in [1, *N*] *and each* $(q_i, v, r_i) \in R_i$, we also have $(q_i, v', r_i) \in R_i$ *for each* $\nu' \in \mathcal{D}^{\mathbf{x}_i}$ *such that* $\nu \downarrow_{\mathbf{x}_i^{\ell}} = \nu' \downarrow_{\mathbf{x}_i^{\ell}}$.

Under this assumption, we use pre-computed data simulations $R_i \subseteq Q_i \times \mathcal{D}^{\mathbf{x}^g} \times Q_i$ and $R_B \subseteq Q_B \times \mathcal{D}^{X_B} \times Q_B$ to generalize the basic subsumption relation between product states (defined by Lemma [4\)](#page-16-2), which may speed up the convergence of Algorithm [1.](#page-17-0)

Lemma 11 *Under Assumption* [3](#page-25-1)*, the relation defined as*

$$
(\langle q_1, \ldots, q_N \rangle, P, \Phi) \subseteq_{\text{sim}} (\langle r_1, \ldots, r_N \rangle, S, \Psi)
$$

\n
$$
\iff
$$

\n
$$
\forall i \in [1, N] \forall \nu \in \mathcal{D}^{\mathbf{x}} \land \vdots \ \nu \models \Phi \Longrightarrow \nu \models \Psi \ and \ \begin{cases} (q_i, \nu \downarrow_{\mathbf{x}_i}, r_i) \in R_i \\ \forall s \in S \exists p \in P : (s, \nu \downarrow_{\mathbf{x}_B}, p) \in R_B \ (2) \end{cases}
$$

is a subsumption relation.

Proof Let $s = (\langle q_1, \ldots, q_N \rangle, P, \Phi)$ and $t = (\langle r_1, \ldots, r_N \rangle, S, \Psi)$ be two product states such that $s \subseteq_{sim} t$. According to Definition [3,](#page-16-1) we need to prove that $\mathcal{L}_s(\mathcal{A}^e \times \overline{B}) \subseteq \mathcal{L}_t(\mathcal{A}^e \times \overline{B})$. For that, it is sufficient to prove that, for each $v \in D^{X,A}$ such that $v \models \Phi$, the following two points hold:

¹⁶ Many realistic systems comply with this restriction, take, for instance, shared-memory multithreading in Java.

1. $\mathcal{L}_{(\langle q_1,...,q_N \rangle,\nu)}(\mathcal{A}^e) \subseteq \mathcal{L}_{(\langle r_1,...,r_N \rangle,\nu)}(\mathcal{A}^e)$, and

2. for all $p \in S$ there exists $q \in P$ such that $\mathcal{L}_{(p,\nu \downarrow_{\mathbf{x}_p})}(B) \subseteq \mathcal{L}_{(q,\nu \downarrow_{\mathbf{x}_p})}(B)$.

Indeed, assuming that the above statements hold, we have

$$
\mathcal{L}_s(\mathcal{A}^e \times \overline{B}) = \bigcup_{v \models \Phi} \left(\mathcal{L}_{(\langle q_1, ..., q_N \rangle, v)}(\mathcal{A}^e) \cap \bigcap_{q \in P} \mathcal{L}_{(q, v \downarrow_{\mathbf{x}_B})}(\overline{B}) \right)
$$
\n
$$
\subseteq \bigcup_{v \models \Phi} \left(\mathcal{L}_{(\langle r_1, ..., r_N \rangle, v)}(\mathcal{A}^e) \cap \bigcap_{r \in S} \mathcal{L}_{(r, v \downarrow_{\mathbf{x}_B})}(\overline{B}) \right)
$$
\n
$$
\subseteq \bigcup_{v \models \Psi} \left(\mathcal{L}_{(\langle r_1, ..., r_N \rangle, v)}(\mathcal{A}^e) \cap \bigcap_{r \in S} \mathcal{L}_{(r, v \downarrow_{\mathbf{x}_B})}(\overline{B}) \right)
$$
\n
$$
= \mathcal{L}_t(\mathcal{A}^e \times \overline{B}),
$$

and we are done. Moreover, the second point above is a direct consequence of the second point of the definition of \mathcal{F}_{sim} and Lemma [9.](#page-22-1) We are left with proving the first point.

To prove the first point, assume that we are given configurations $(\langle q_0^1, \ldots, q_0^N \rangle, v_0)$ and $((r_0^1, \ldots, r_0^N), v_0)$ of \mathcal{A}^e such that $\forall i \in [1, N] : (q_0^i, v_0 \downarrow_{\mathbf{x}_i}, r_0^i) \in R_i$. We show that if there is a run $(\langle q_0^1, \ldots, q_0^N \rangle, v_0) \stackrel{\sigma_0}{\rightarrow} \cdots \stackrel{\sigma_{n-1}}{\rightarrow} (\langle q_n^1, \ldots, q_n^N \rangle, v_n)$ for any $n \ge 0$, then there is some run $(\langle r_0^1, \ldots, r_0^N \rangle, v_0) \stackrel{\sigma_0}{\rightarrow} \cdots \stackrel{\sigma_{n-1}}{\longrightarrow} (\langle r_n^1, \ldots, r_n^N \rangle, v_n)$ where $\forall i \in [1, N] : \forall j \in$ $[0, n]: (q_j^i, v_j \downarrow_{\mathbf{x}_i}, r_j^i) \in R_i \wedge (q_j^i \in F_i \Longrightarrow r_j^i \in F_i)$, by induction on the length *n* of the run. The base case for $n = 0$ follows trivially from the assumption $\forall i \in [1, N] : (q_0^i, v_0 \downarrow_{\mathbf{x}_i}, r_0^i) \in$ R_i and from the first point of the definition of data simulations (Def. [4\)](#page-22-0). Now, assuming that the property holds for runs of length *n*, we show that it holds for runs of length *n*+1 too. Take a run $(\langle q_0^1, \ldots, q_0^N \rangle, \nu_0) \stackrel{\sigma_0}{\rightarrow} \cdots \stackrel{\sigma_{n-1}}{\rightarrow} (\langle q_n^1, \ldots, q_n^N \rangle, \nu_n) \stackrel{\sigma_n}{\rightarrow} (\langle q_{n+1}^1, \ldots, q_{n+1}^N \rangle, \nu_{n+1}).$ Further, take a configuration $((r_0^1, \ldots, r_0^N), v_0)$ such that $\forall i \in [1, N] : (q_0^i, v_0 \downarrow_{\mathbf{x}_i}, r_0^i) \in R_i$. From the induction hypothesis, we immediately get that there exists a run $(\langle r_0^1, \ldots, r_0^N \rangle, v_0) \stackrel{\sigma_0}{\rightarrow}$ $\dots \xrightarrow{\sigma_{n-1}} (\langle r_n^1, \dots, r_n^N \rangle, v_n)$ where $\forall i \in [1, N] : \forall j \in [0, n] : (q_j^i, v_j \downarrow_{\mathbf{x}_i}, r_j^i) \in R_i \wedge (q_j^i \in$ *F_i* \implies *r*^{*i*}</sup> ∈ *F_i*). Next, let *I* ⊆ [0, *N*] be the set of indices of the GRAs that make a move during the step $(\langle q_n^1, \ldots, q_n^N \rangle, v_n) \stackrel{\sigma_n}{\longrightarrow} (\langle q_{n+1}^1, \ldots, q_{n+1}^N \rangle, v_{n+1})$. For any $i \in I$, from $(q_n^i, v_n \downarrow_{\mathbf{x}_i}, r_n^i) \in R_i$ and $(q_n^i, v_n \downarrow_{\mathbf{x}_i}) \xrightarrow{\sigma_n} (q_{n+1}^i, v_{n+1} \downarrow_{\mathbf{x}_i})$, we get there there is some r_{n+1}^i such that $(r_n^i, v_n \downarrow_{\mathbf{x}_i}) \stackrel{\sigma_n}{\rightarrow} (r_{n+1}^i, v_{n+1} \downarrow_{\mathbf{x}_i}), (q_{n+1}^i, v_{n+1} \downarrow_{\mathbf{x}_i}, r_{n+1}^i) \in R_i$, and $q_{n+1}^i \in F_i \rightarrow$ $r_{n+1}^i \in F_i$. Moreover, for any $i \in [1, N] \setminus I$, the fact that $v_n \downarrow_{\mathbf{x}_i^i} = v_{n+1} \downarrow_{\mathbf{x}_i^i}$, $(q_n^i, v_n \downarrow_{\mathbf{x}_i}, r_n^i) \in$ R_i , $q_{n+1}^i = q_n^i$, $r_{n+1}^i = r_n^i$, and Assumption [3,](#page-25-1) give us $(q_{n+1}^i, v_{n+1} | \mathbf{x}_i, r_{n+1}^i) \in R_i$, and, consequently, $q_{n+1}^i \in F_i \to r_{n+1}^i \in F_i$ too.

6 Experimental results

We have implemented both Algorithm [1](#page-17-0) (trace inclusion) and Algorithm [2](#page-23-0) (data simulations) in a prototype tool INCLUDER¹⁷ using the MATHSAT SMT solver [\[9\]](#page-32-24) for answering the satisfiability queries and computing the interpolants. The results of the experiments with trace inclusion are given in Tables [1](#page-27-0) and [2.](#page-28-0) The results of experiments combining trace inclusion and simulations are given in Table [3.](#page-30-0) The results were obtained on an Intel i7-4770 CPU @ 3.40GHz machine with 32GB RAM.

¹⁷ [http://www.fit.vutbr.cz/research/groups/verifit/tools/includer/.](http://www.fit.vutbr.cz/research/groups/verifit/tools/includer/)

6.1 Trace inclusion

Table [1](#page-27-0) contains experiments where the network *A* consists of a single component.We applied the tool on several verification conditions generated from imperative programs with arrays [\[8](#page-32-5)] (Array shift, Array rotation 1+2, Array split) available online [\[31](#page-33-3)]. Then, we applied it on models of hardware circuits (HW Counter 1+2, Synchronous LIFO) [\[33](#page-33-4)]. Finally, we checked two versions (correct and faulty) of the timed Alternating Bit Protocol [\[36\]](#page-33-5).

Table [2](#page-28-0) provides a list of experiments where the network A has $N > 1$ components. First, we have the example of Fig. [1](#page-2-0) (Running). Next, we have several examples of real-time verification problems [\[34](#page-33-0)]: a controller of a railroad crossing [\[23](#page-32-27)] (Train) with *T* trains, the Fischer Mutual Exclusion protocol with deadlines Δ and Γ (Fischer), and a hardware communication circuit with *K* stages, composed of timed NOR gates (Stari). Third, we have modeled a Producer–Consumer example [\[15\]](#page-32-28) with a fixed buffer size *B*. Fourth, we have experimented with several models of parallel programs that manipulate arrays (Array init, Array copy, Array join) with window size Δ .

For the time being, our implementation is a proof-of-concept prototype that leaves plenty of room for optimization (e.g., caching of intermediate computation results) likely to improve the performance on more complicated examples. Despite that, we found the results from Tables [1](#page-27-0) and [2](#page-28-0) rather encouraging.

6.2 Combination of trace inclusion and simulations

Unlike the computation of the most general simulation on a finite-alphabet automaton, which is possible in polynomial time $[20]$, computing the weakest data simulation on a GRA is, in general, impossible due to the fact that the data constraints cannot be represented in a decidable logical domain, such as linear integer arithmetic. For this reason, our algorithm (Algorithm [2\)](#page-23-0) is sound but incomplete, returning a possibly stronger simulation, in which the data constraint associated with certain pairs of states is set of ⊥. Such simulations can be computed in reasonable time, but they could be of limited use in speeding up the antichainbased trace inclusion check.

Our implementation tries to achieve a balance between these opponent goals, as shown by the results in Table [3.](#page-30-0) We apply a timeout on each single call of the *PreSim* function in

Algorithm [2.](#page-23-0) Moreover, we may also limit the size of the resulting formula^{[18](#page-29-1)} for a single call of the *PreSim* function. If the timeout or the size limit is exceeded, the result of *PreSim* will be safely underapproximated to \bot (i.e., no simulation).

The use of simulation-based subsumption has an impact on running times of the trace inclusion in the examples, where (i) the system contains a nontrivial simulation relation, which could be discovered by Algorithm [2](#page-23-0) with at most $K = 2$ iterations on each pair of states and (ii) some product states in the antichain tree are compatible with this simulation relation. This is clearly visible in the Fischer 2-serial and Fischer 2-branching models where the synchronization based on the Fischer protocol is used in non-minimalistic scenarios, i.e., scenarios not restricted to a single critical section. In particular, Fischer 2-serial is an abstract model of a system where a process uses the Fischer protocol to access a critical section twice in a row. Fischer 2-branching is an abstraction of a system where a single process contains two branches and each of these branches accesses a critical section using the Fischer's protocol. The parameters Δ and Γ are parameters of the Fischer's protocol, and N is the number of parallel processes. Note that if the system contains a counterexample (e.g. Fischer 2-serial with parameters $\Delta = 2$, $\Gamma = 1$, $N = 3$), the simulation-based subsumption may also increase the running time. The reason is that the computation is stopped when an accepting product state is discovered and the rest of the antichain tree is not constructed. The simulationbased subsumption makes the whole antichain tree smaller (in terms of nodes), but the shortest counterexample path may be subsumed by a longer one resulting to a postpone of a counterexample discovery.

Also note that we managed to compute nontrivial simulations¹⁹ for all the examples from Tables [1](#page-27-0) and [2.](#page-28-0) However, in most of them, the use of the simulation has no impact on the time of checking the trace inclusion. The main reason is that most of the protocols are modeled by automata where very limited data simulations exist between pairs of states (i.e., the data constraints under which the simulation holds are quite strong), and product states in the antichain tree are incompatible with these simulations (cf. Points 1 and 2 of Lemma [11\)](#page-25-2).

7 Conclusions

We have presented an interpolation-based abstraction refinement method for trace inclusion between a network of generic register automata and an observer where the variables used by the observer are a subset of those used by the network. The procedure builds on a new determinization result for GRAs and combines in a novel way predicate abstraction and interpolation with antichain-based inclusion checking. The efficiency of the basic method can be further enhanced by data simulations. The procedure has been successfully applied to several examples, including verification problems for array programs, real-time systems, and hardware designs.

For the future, it is interesting to extend the method to data tree automata and apply it to logics for heaps with data. Also, we foresee an extension of the method to handle infinite traces. Finally, it is also an open problem how to handle the case when the observer is allowed to have local variables.

¹⁸ The size of a formula is measured in the number of nodes of its MathSAT graph-based representation.

¹⁹ A simulation *R* is trivial iff $\forall x, y \in Q : x \neq y \rightarrow (x, \perp, y) \in R$.

Table 3 Experiments with simulations

un or me is the parameter of Algorithm [2,](#page-23-0) *Max size* is a limit on the size of the result of the ß $\frac{1}{2}$ J mmmmm .
5 $\frac{1}{2}$ the par \overline{a} *K*based subsumption, and *Timesim* represents the time needed to compute data simulations. based *industryption*, and *T ime_{sim}* represents the time needed to compute data single *PreSim* function *PreSim* function *PreSim* function, and *TO* is a timeout on a single call of the *PreSim* function

Appendix A: Alternative notions of the product state

Below, we briefly discuss two alternative notions of product states that we originally considered but dropped them since we were not able to build a sound antichain construction on them.

The first option we considered was to link predicates with the *individual* states involved in a product state. In that case, the predicate map linked particular states of automata *^A^e* and *B* to sets of formulas as follows: $\Pi_{ind}: Q_{\mathcal{A}^e} \cup Q_B \rightarrow 2^{\text{Form}(\mathcal{D})}$. The product state was then defined as $s_{ind} = (\langle \mathbf{q}, \Phi_a \rangle, P)$ with **q** being a state of the automaton $\mathcal{A}^e, \Phi_a \subseteq \Pi_{ind}(\mathbf{q})$, and $P \subseteq \{ \langle r, \Phi_r \rangle \mid r \in Q_B \text{ and } \Phi_r \subseteq \Pi_{ind}(r) \}.$ The semantics of the product state $s_{ind} = (\langle \mathbf{q}, \Phi_a \rangle, P)$ was that whenever the automaton A^e is in the state **q** with a valuation $\nu \models \Phi_a$ of the variables, then the automaton *B* can be in any state *r* such that $\langle r, \Phi_r \rangle \in P$ and $\nu \models \Phi_r$. A product state $s_{ind} = (\langle \mathbf{q}, \Phi_q \rangle, P)$ was considered accepting iff $\mathbf{q} \in F_{\mathcal{A}^e}$ and there existed $\nu \models \Phi_q$ such that $\nu \not\models \bigvee{\lbrace \Phi_r | \langle r, \Phi_r \rangle \in P \land r \in F_B \rbrace}$. That implied existence of a trace accepted by A^e at the state **q** with the final valuation *v*, not covered by the automaton *B*. A problem with this product construction is that it cannot be used for soundly deciding the inclusion problem as shown in the following example: Take the product state $s_1 = (\langle q_1, x \in \{1, 2\}\rangle, \{ \langle r_1, x = 1 \rangle, \langle r_2, x = 2 \rangle \})$ obtained for an automaton \mathcal{A}^e with the rule $q_1 \xrightarrow{\sigma, x' = x+1} q_2$ and an automaton *B* with rules $r_1 \xrightarrow{\sigma, x' = x} r_3$ and $r_2 \xrightarrow{\sigma, x' = x+1 \wedge x>10} r_3$. Moreover, let q_2 be final in A^e and r_3 be final in *B*. When one computes the post of s_1 , one gets $s_2 = (\langle q_2, x \in \{2, 3\} \rangle, \{ \langle r_3, x > 1 \rangle \})$, which is not accepting, because all configurations of A^e (i.e. $x \in \{2, 3\}$) are covered by configurations of *B* (i.e. $x > 1$). However, the automaton A^e can do a step $(q_1, x = 2) \xrightarrow{\sigma, x' = x+1} (q_2, x = 3)$, which cannot be followed by *B* (it cannot do a step from the configurations $(r_1, x = 2)$ or $(r_2, x = 2)$). Hence, an antichain construction based on this notion of product states could hide a real counterexample and provide an unsound answer.

In order to avoid the unsoundness of the above solution, we attempted to use predicates representing *relations* between successive values of variables within a step leading to a given product state. In this case, the predicate map was defined as Π_{rel} : $Q_{\mathcal{A}^e} \cup Q_{\mathcal{B}} \rightarrow$ $2^{\text{Form}(\mathcal{D})} \times 2^{\text{Form}(\mathcal{D})}$. The product state was then defined as $s_{rel} = (\langle \mathbf{q}, \Phi_q \rangle, P)$ with **q** being a state of the automaton \mathcal{A}^e , $\Phi_q \subseteq \Pi_{rel}(q)$, and $P \subseteq \{\langle r, \Phi_r \rangle \mid r \in Q_B \text{ and } \Phi_r \subseteq \Pi_{rel}(r)\}.$ The semantics of the product state $s_{rel} = (\langle \mathbf{q}, \Phi_q \rangle, P)$ was that whenever the last step of A^e was $($, *v* $)$ \rightarrow $($ **q**, *v'* $)$ such that $($ *v*, *v'* $)$ \models Φ *r*, then the last step of *B* could have been $($ _L, ν $)$ \rightarrow (r, v') where $\langle r, \Phi_r \rangle \in P$ and $(\nu, \nu') \models \Phi_r$. (The source states of the steps were not reflected in the product states, and hence are represented using the underscore sign.) A product state was considered final iff $\mathbf{q} \in F_{\mathcal{A}^e}$ and there existed a relation $(v, v') \models \Phi_r$ such that $(v, v') \not\models \bigvee \{\Phi_r \mid \langle s, \Phi_r \rangle \land r \in F_B\}$. The antichain tree could be used for sound checking of the inclusion in this case. However, a problem was to find a subsumption relation to soundly prune the antichain tree. A natural way of defining the subsumption relation following the approach of [\[1](#page-32-12)] is to define the subsumption as follows: $(\langle q_1, \Phi_1 \rangle, P_1) \sqsubseteq (\langle q_2, \Phi_2 \rangle, P_2)$ iff (i) $\mathbf{q}_1 = \mathbf{q}_2$, (ii) $\Phi_1 \to \Phi_2$, and (iii) for each $\langle r, \Phi_r \rangle \in P_2$ there exists $\langle s, \Phi_s \rangle \in P_1$ such that $r = s$ and $\Phi_r \to \Phi_s$. Unfortunately, it turns out that using such a subsumption cannot be used for sound inclusion checking since comparing formulae representing solely the last step of the automata can lead to omitting counterexamples to inclusion that depend on longer traces. Existence of a suitable sound subsumption for this type of product states, which is needed to ensure termination of the antichain construction, is an open problem.

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